

EC_IT8511TE SETTING

Pin	Pin Name	Signal Name	Type
32	PWM0/GPA0	LCD_BL_PWM	O
33	PWM1/GPA1	FAN_PWM	O
36	PWM2/GPA2	/	
37	PWM3/GPA3	/	
38	PWM4/GPA4	CHG_LED_UP#	O
39	PWM5/GPA5	PWR_LED_UP#	O
40	PWM6/GPA6	/	
43	PWM7/GPA7	LCD_BACKOFF#	O
153	RXD/GPB0	NUM_LED	O
154	TXD/GPB1	CAP_LED	O
162	GPB2	SCRL_LED	O
163	SMCLK0/GPB3	SMB0_CLK	IO
164	SMDAT0/GPB4	SMB0_DAT	IO
5	GA20/GPB5	A20GATE	O
6	KBRST#/GPB6	RC_IN#	O
165	GPB7	THRO_CPU	O
47	CLKOUT/GPC0	/	
169	SMCLK1/GPC1	SMB1_CLK	IO
170	SMDAT1/GPC2	SMB1_DAT	IO
171	GPC3	/	
172	TMRI0/WUI2/GPC4	AC_IN_OC#	I
175	GPC5	OP_SD#	O
176	TMRI1/WUI3/GPC6	BAT1_IN_OC#	I
1	CK32KOUT/GPC7	/	O
26	RI1#/WUI0/GPD0	PM_SUSB#	I
29	RI2#/WUI1/GPD1	PM_SUSC#	I
30	LPCRST#/WUI4/GPD2	BUF_PLT_RST#	I
31	ECSC#/GPD3	EXT_SC#	O
41	GPD4	RFON_SW#	I
42	GINT/GPD5	PM_SLP_M#	I
62	TACH0/GPD6	FAN0_TACH	I
63	TACH1/GPD7	COLOREN#	O
87	ADC4/GPE0	/	I
88	ADC5/GPE1	INTERNET#	I
89	ADC6/GPE2	MARATHON#	I
90	ADC7/GPE3	DISTP#	I
2	PWR5W/GPE4	PWR_SW#	I
44	WUI5/GPE5	/	
24	LPCPD#/WUI6/GPE6	ME_ALERT#	I
25	CLKRUN#/WUI7/GPE7	PM_CLKRUN#	
110	PS2CLK0/GPF0	/	
111	PS2DAT0/GPF1	/	
114	PS2CLK1/GPF2	/	IO
115	PS2DAT1/GPF3	/	IO
116	PS2CLK2/GPF4	TP_CLK	
117	PS2DAT2/GPF5	TP_DAT	
118	PS2CLK3/GPF6	/	
119	PS2DAT3/GPF7	INSTANT_ON#	I
113	FA16/GPG0	FA16	
112	FA17/GPG1	FA17	
104	FA18/GPG2	FA18	
103	FA19/GPG3	FA19	
3	FA20/GPG4	LID_SW#	I
4	FA21/GPG5	/	
27	LPC80HL/GPG6	PM_THERM#	O
28	LPC80HL/GPG7	AC_APR_UC#	I

Pin	Pin Name	Signal Name	Type
8	GPL0	PM_S4_STATE#	I
11	GPL1	S4_STATE_ON	O
12	GPL2	SLP_M_ON	O
20	GPL3	EC_WLAN_PWR	O
21	GPL4	MP_PWRGD	I
48	PH0	VSUS_ON	I
54	PH1	SUS_PWRGD#	O
55	PH2	CPUPWR_GD#	O
69	PH3	PM_PWRBTN#	O
70	PH4	SUSC_EC#	O
75	PH5	SUSB_EC0#	O
76	PH6	CPU_VRON	O
105	PH7	PM_RSMRST#	O
148	GP10	ICH0_PWROK	O
149	GP11	ALL_SYSTEM_PWRGD	I

ICH8-M GPIO SETTING

Pin	Pin Name	Signal Name	Type
AG12	GPIO0/BM_BUSY#	PM_BMBUSY#	I
AJ8	GPIO1/TACH1	BT_DET#	I
F8	GPIO2/PIRQ#	PCI_INTE#	I
G11	GPIO3/PIRQ#	PCI_INTF#	I
F12	GPIO4/PIRQ#	PCI_INT#	I
B3	GPIO5/PIRQ#	PCI_INT#	I
AJ9	GPIO6/TACH2	BIOS_REC	IO
AH9	GPIO7/TACH3	WLAN_LED_ON	I
AE16	GPIO8	EXT_SM#	I
AG19	GPIO9/WOL_EN	LAN_WOL_EN	I
AJ24	GPIO10/CLGPI01	ME_ALERT#	O
AG22	GPIO11/SMBALERT#	SMB_ALERT#	I
AC19	GPIO12	EXT_SC#	I
AH21	GPIO13/GLAN_DOCK#	ENERGY_DET	I
AF22	GPIO14/CLGPI02	NETDETECT	
AE20	GPIO15/STP_PC#	STP_PC#	IO
AJ14	GPIO16/DPRSLPVR	PM_DPRSLPVR	O
AG8	GPIO17/TACH0	WLAN_ON#	O
AH12	GPIO18	/	
AJ10	GPIO19/SATA1GP	GPIO19	I
AE11	GPIO20	BTLED_ON	O
AJ12	GPIO21/SATA0GP	CPU_Select	I
AG10	GPIO22/SCLOCK	/	
E6	GPIO23/LDRQ1#	LPC_DRQ#1	IO
AJ27	GPIO24/CLGPI00	LAN_PHYPC_R	O
AG18	GPIO25/STP_CPU#	STP_CPU#	
AH27	GPIO26/S4_STATE#	PM_S4_STATE#	
AH25	GPIO27/ORT_STATE0	BT_ON#	I
AD16	GPIO28/ORT_STATE1	CB_SD#	
AG17	GPIO29/OC#5	USB_OC#5	I
AD12	GPIO30/OC#6	NEWCARD_OC#	I
AJ18	GPIO31/OC#7	USB_OC#7	I
AH11	GPIO32/CLKRUN#	PM_CLKRUN#	O
AE10	GPIO33/AZ_DOCK_EN#		O
AG14	GPIO34/AZ_DOCK_RST#		
AG13	GPIO35/SATACLKREQ#	GPO35(SATA_CLKREQ#)	
AF11	GPIO36/SATA2GP	GP06	I
AG11	GPIO37/SATA3GP	PCB_ID0	I
AF9	GPIO38/SLOAD	PCB_ID1	I
AJ11	GPIO39/SDATAOUT0	PCB_ID2	I
AD10	GPIO40/SDATAOUT1	/	
AG29	GPIO49/CPUPWRGD	CPUPWRGD	I

Pin	Pin Name	Signal Name	Type
152	GP12	/	
155	GP13	CHG_EN#	O
168	GP15	EC_CLK_EN	O
85	KS016/GPM2	NETDETECT	O
86	KS017/GPM3	LAN_WOL_EN	I

PCI Device	IDSEL#	REQ/GNT#	Interrupts
CARD READER	AD17	0	A
1394	AD17	0	B

PCIE Device	Bus	PCIE Device	Bus
LAN	PE(T/R)(p/n)1	Robson	PE(T/R)(p/n)4
Kedron	PE(T/R)(p/n)2	NEWCARD	PE(T/R)(p/n)5
eSATA	PE(T/R)(p/n)3	GLAN	PE(T/R)(p/n)6

SM-Bus Device	SM-Bus Address
Clock Generator	1101001x (D2)
SO-DIMM 0	1010000x (A0)
SO-DIMM 1	1010001x (A2)
CPU Thermal Sensor(MAX6657)	0100110x (4C)
VGA Thermal Sensor(MAX6657)	0100110x (4C)

ICS9LPR363AGLF-T SETTING

Pins	Pin Name	Device
3	PCICLK1	TPMPCI
4	PCICLK2	CardBus R5C832
5	SELPCIEX0_LCD#PCICLK3	EC IT8511E
8	ITP_EN/PCICLK_F0	ICH8
15	DOTC_96MHzL	X
14	DOTT_96MHzL	X
12	FSLA/USB_48MHz	ICH8
19	PCieT_L1	eSATA
20	PCieC_L1	eSATA
36	PCieT_L5	M66M
35	PCieC_L5	M66M
39	PCieT_L6	MiniCard(Rob)
38	PCieC_L6	MiniCard(Rob)
22	PCieT_L2	ICH8
23	PCieC_L2	ICH8
24	PCieT_L3	MCH
25	PCieC_L3	MCH
26	SATACLKT_L	ICH8
27	SATACLKC_L	ICH8
44	CPUITPTC_L2 / PCieT_L8	LAN
43	CPUITPTC_L2 / PCieC_L8	LAN
14	27FIX/LCD_SSCTG/PCieT_L0	M66M
15	DOTC_96MHzL	M66M
41	PEREQ1# / PCieT_L7	MiniCard
40	PEREQ2# / PCieC_L7	MiniCard
32	PEREQ3#	NewCard
33	PEREQ4#	MiniCard
30	PCieT_L4	NewCard
31	PCieC_L4	NewCard
49	CPUT_L1F	MCH
48	CPUC_L1F	MCH
52	CPUT_L0	CPU
51	CPUC_L0	CPU
57	X2	14.318MHz
58	X1	14.318MHz
60	REF0	ICH8
61	REF17/FSLC/TEST_SEL	X

ASUS		Title : <Title>	
ASUSTek Computer INC. NBI		Engineer: N/A	
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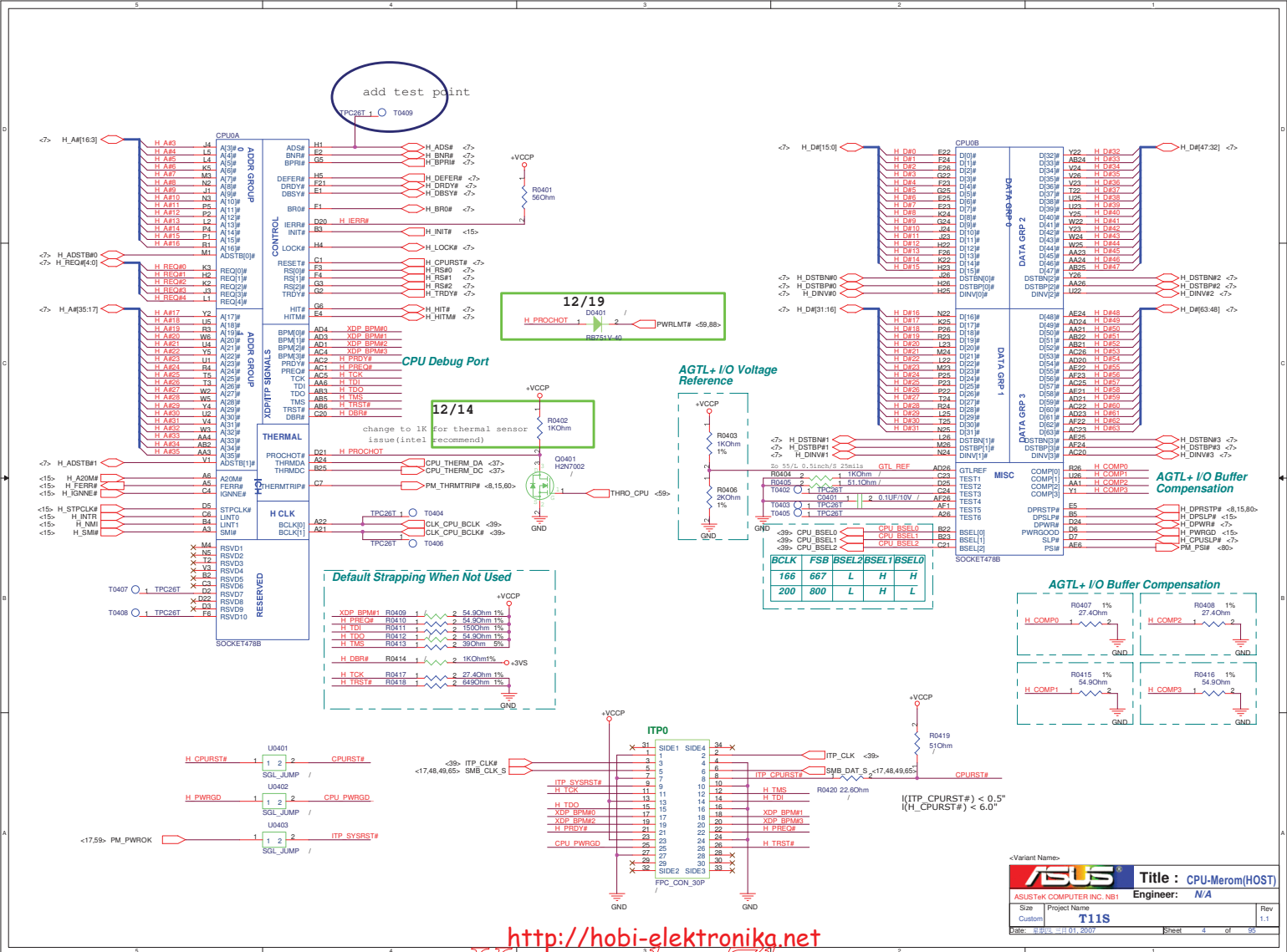
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- 1.H7415 , H7417 , H7419改?有潤濕點的type。
- 2.WLAN_ON#(GPIO17) change to +3VS pull High
- 4.R5950,R5920 delte,重復上拉。Delete Q3901,Q3902
- 5.Delete the thermal sensor if GPU could get it's tempture by itself.
- 6.D6201 5pin power 改?在bead 后
- 8.SW7801 change to DIP at BOM
- 7.Delete D5102
- 9.82566 errata:add 10nf between +3V and 1.8vctrl and +1.0Vctrl
- 10.R2460,R2422,R2426 unstuff,R2323 pull Down
- 11.Add C2003,CN2201,CN2202,CN2206
- 12.SPI ROM P/N change to:05G001405010

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		Engineer: N/A	
ASUSTeK COMPUTER INC.			
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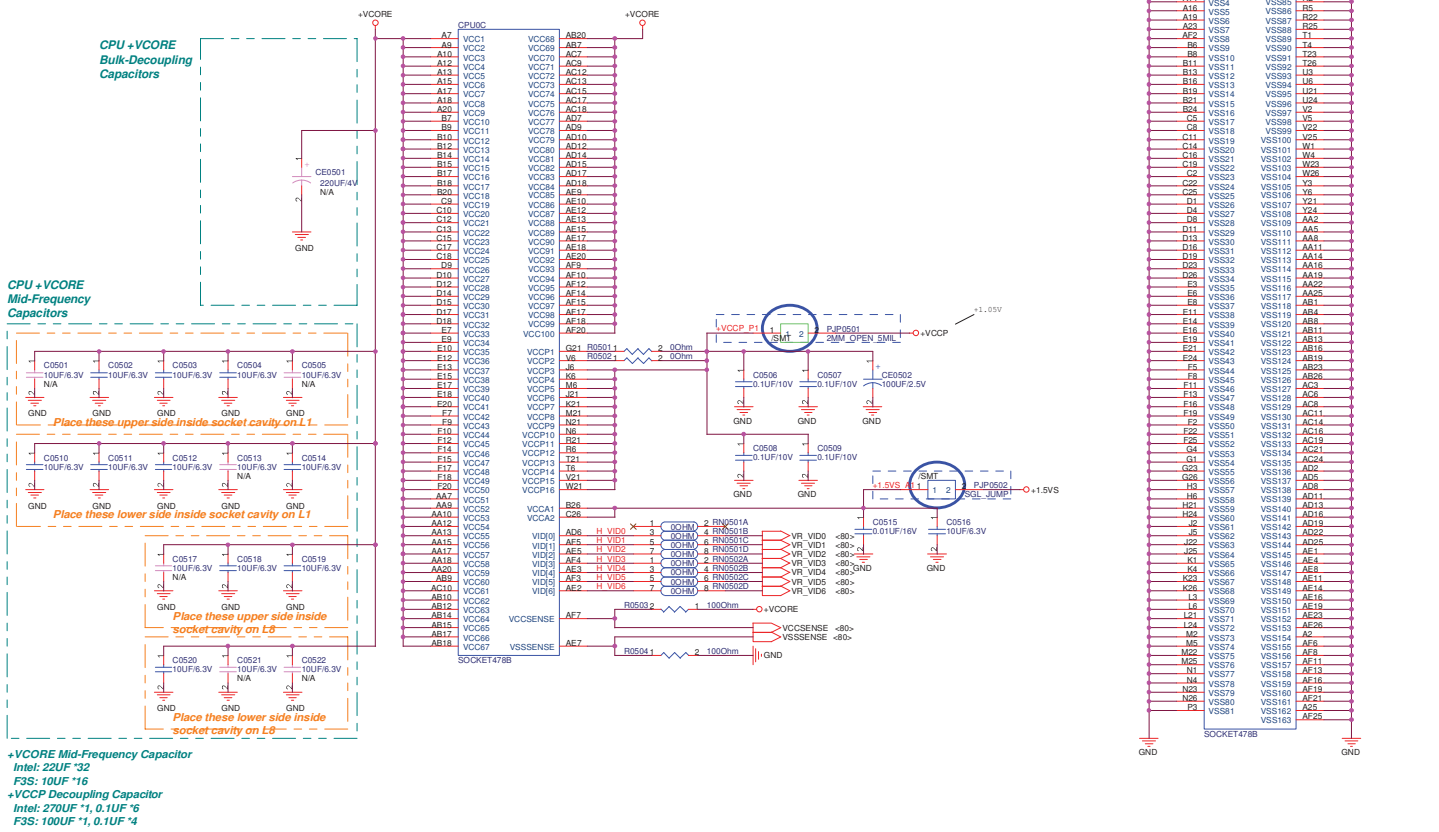
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For Calibrating the FSB I/O Buffer



For Slew Rate Compensation on the FSB

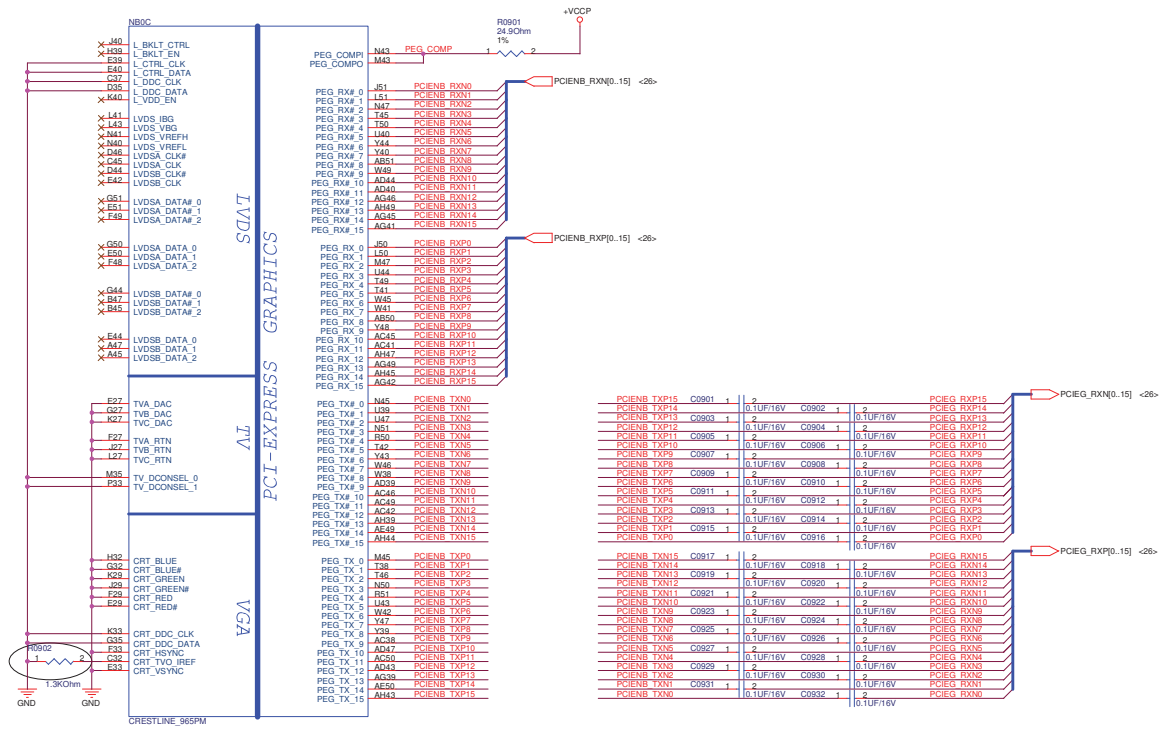


For Providing a Reference Voltage to The FSB RCOMP circuits



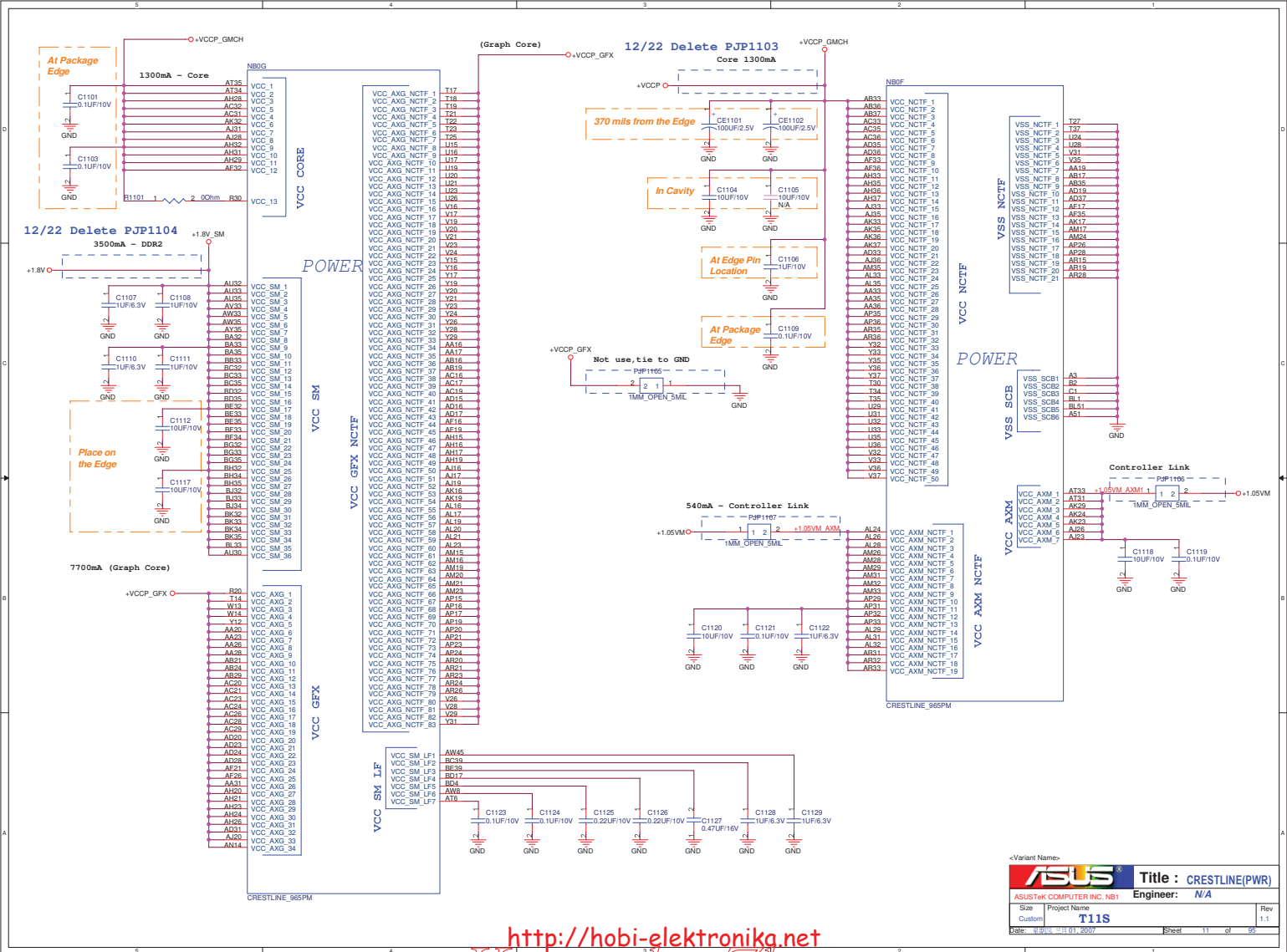
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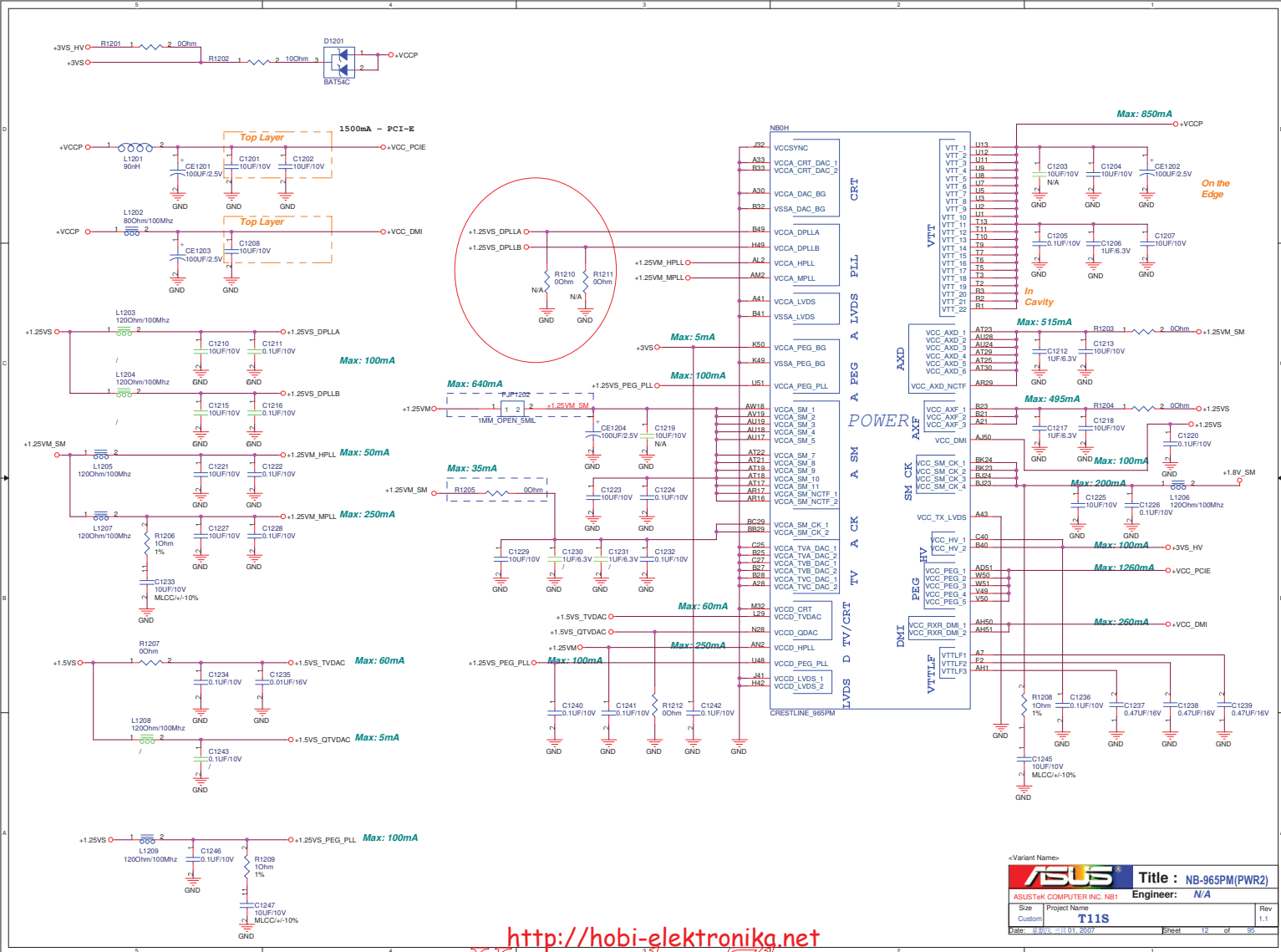
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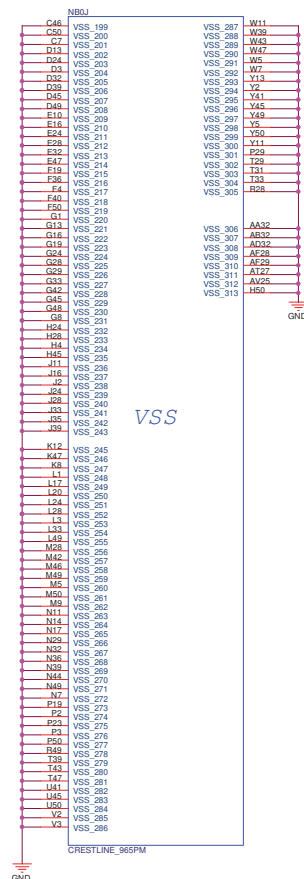
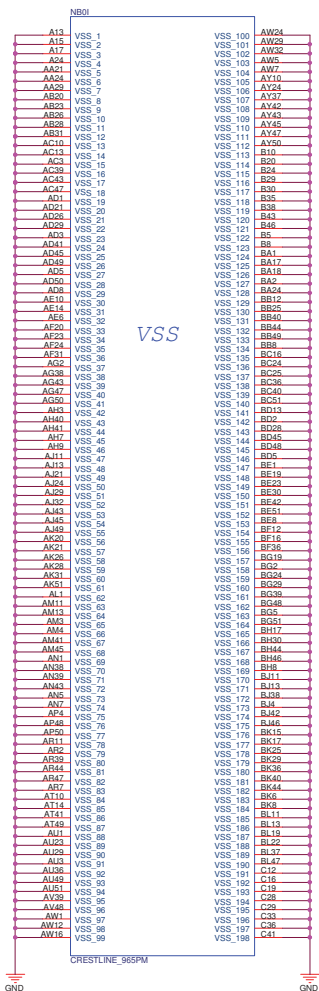
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ASUS Title : CRESTLINE(GND)

ASUSTek COMPUTER INC. NB1 Engineer: N/A

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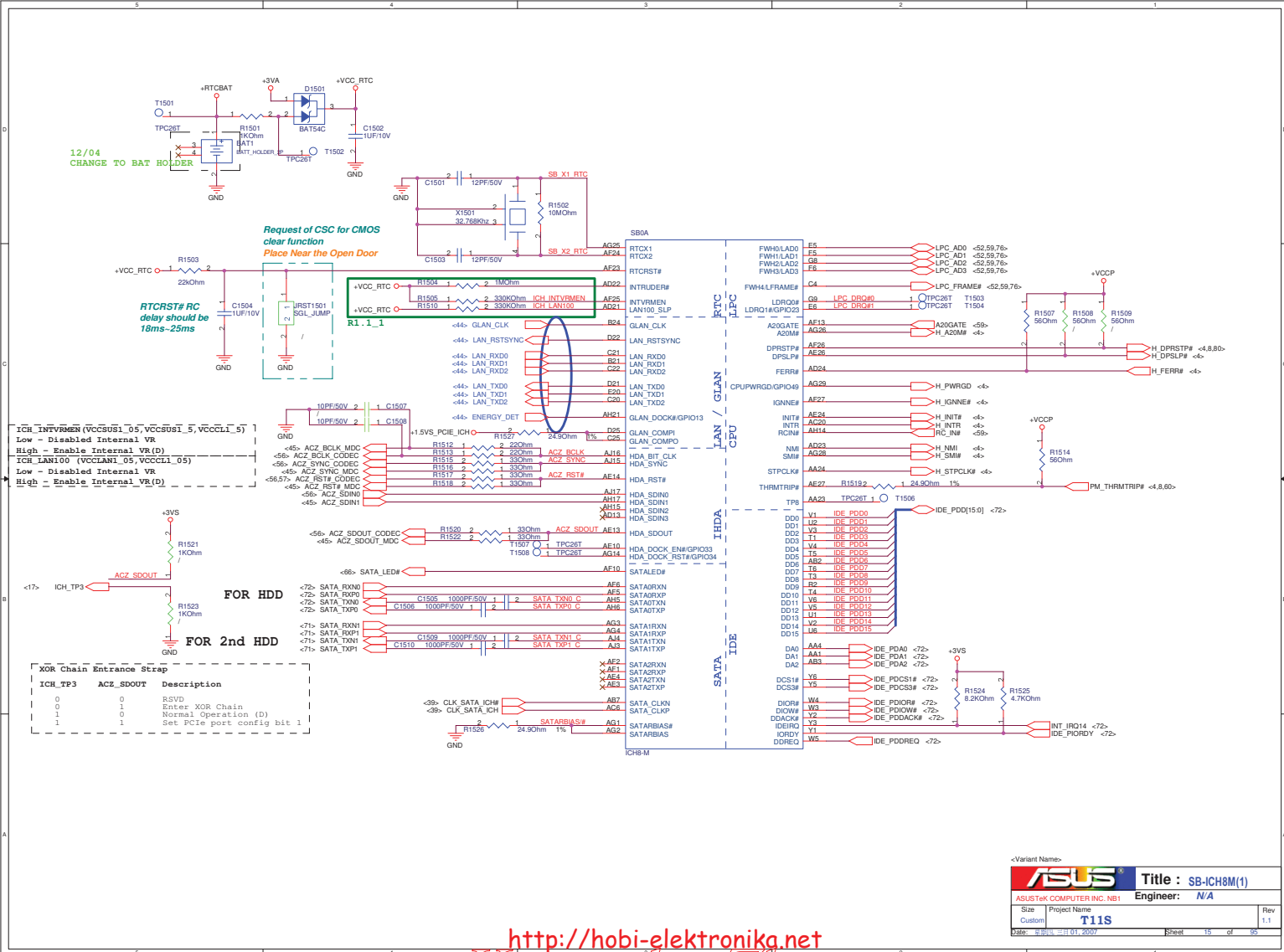
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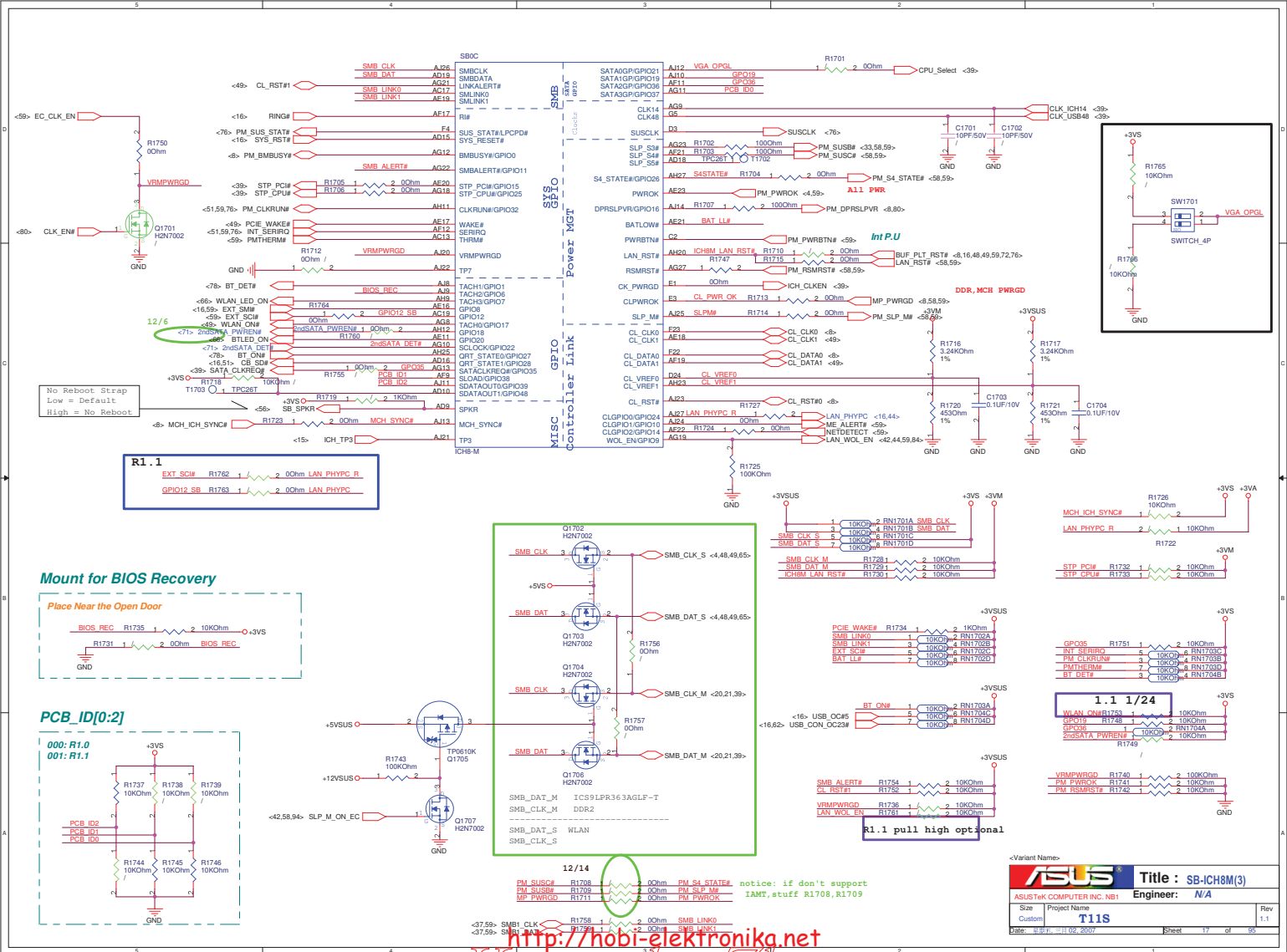
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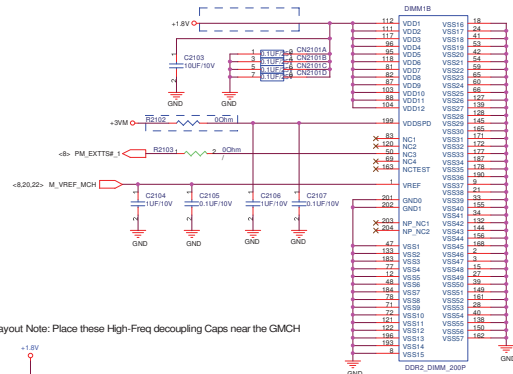


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		DMM1A																DMM1B																																																																																																																																																																																																																																																																																																												
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A circuit diagram showing a 1.8V voltage source connected to a series of four capacitors (C2108, C2109, C2110, C2111) connected to ground. Each capacitor is labeled with its value: 0.1UF/10V. The capacitors are connected in parallel to ground, and the voltage source is connected to the top of the first capacitor (C2108).

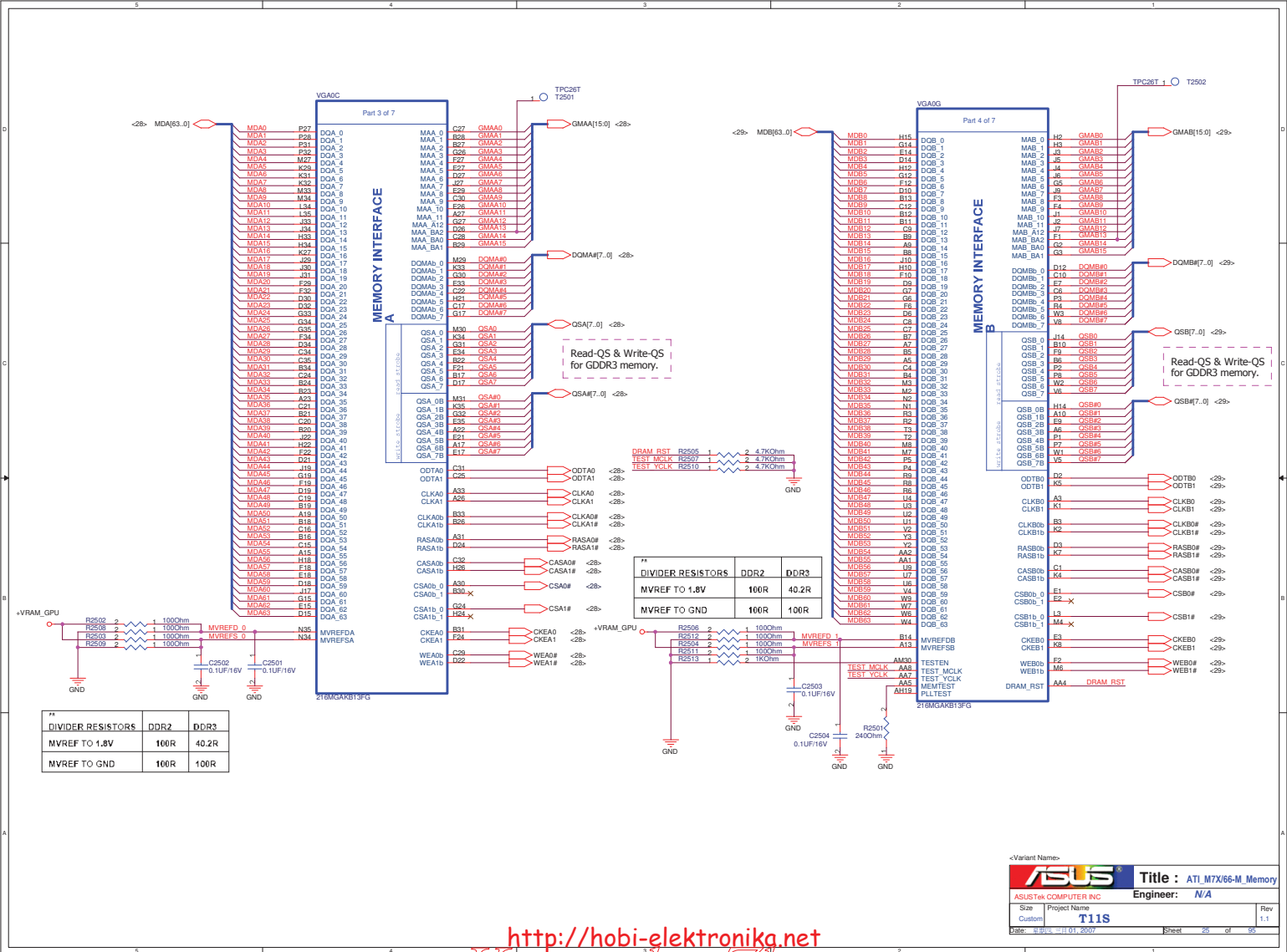
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1.1 1/24 add CAP

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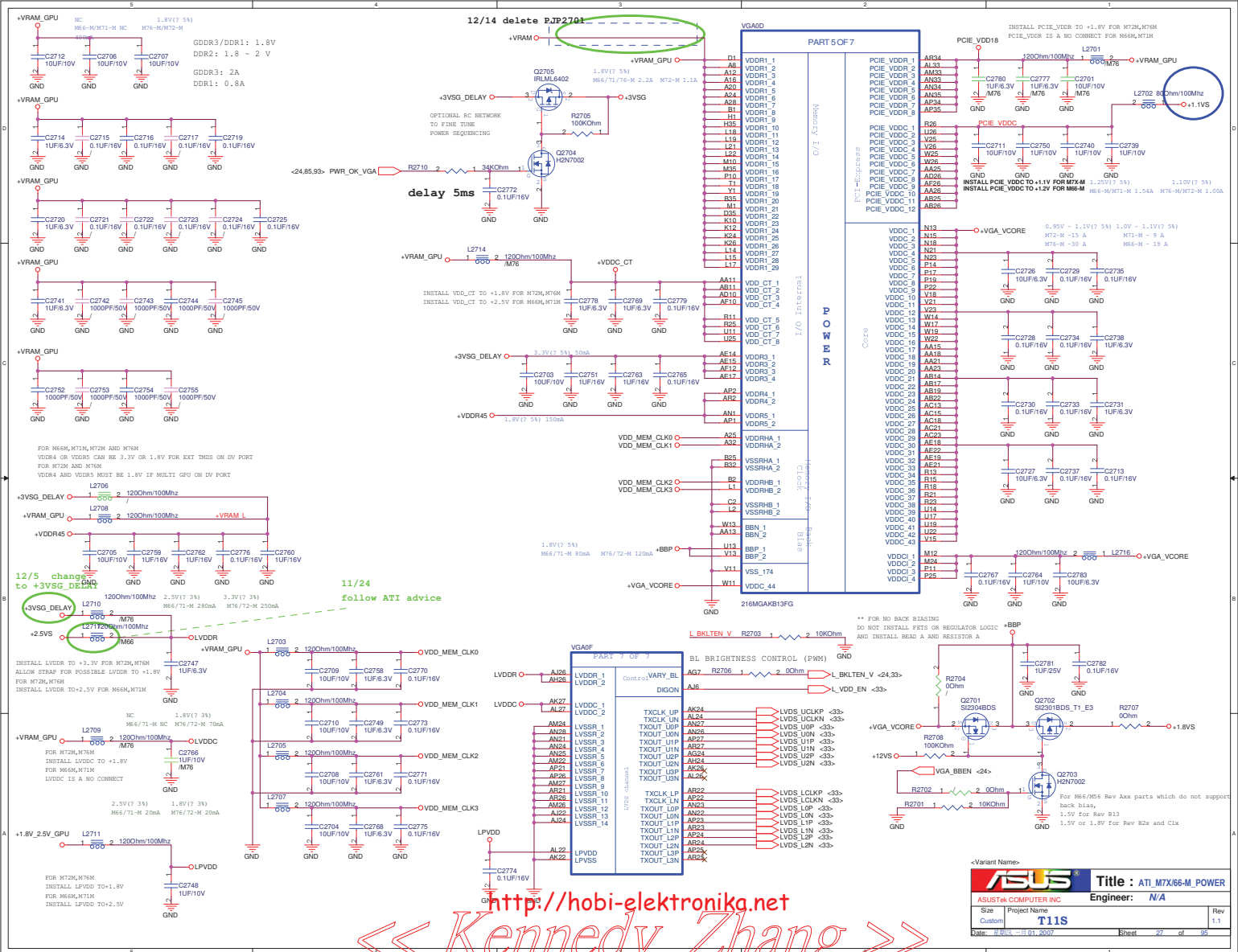
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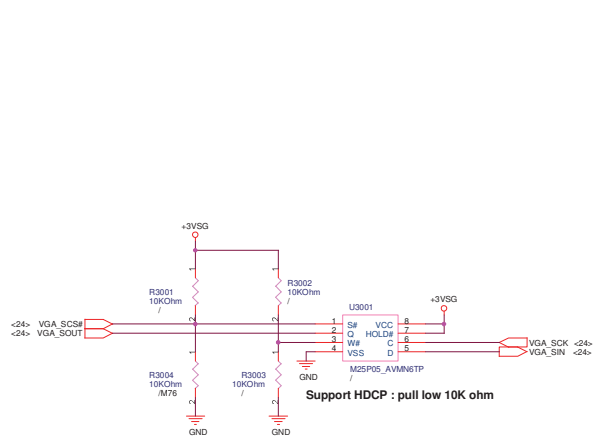
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<http://hobi-elektronika.net>

<< Kennedy_Zhang >>

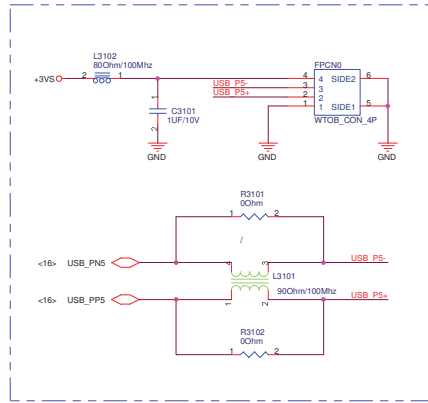




<Variant Name>		ASUS		Title : VGA SPI	
ASUSTek COMPUTER INC		Engineer: N/A			
Size	Project Name	T11S		Rev	1.1
Custom				Sheet	90 of 95
Date: 2010.01.01		2007			

<http://hobi-elektronika.net>

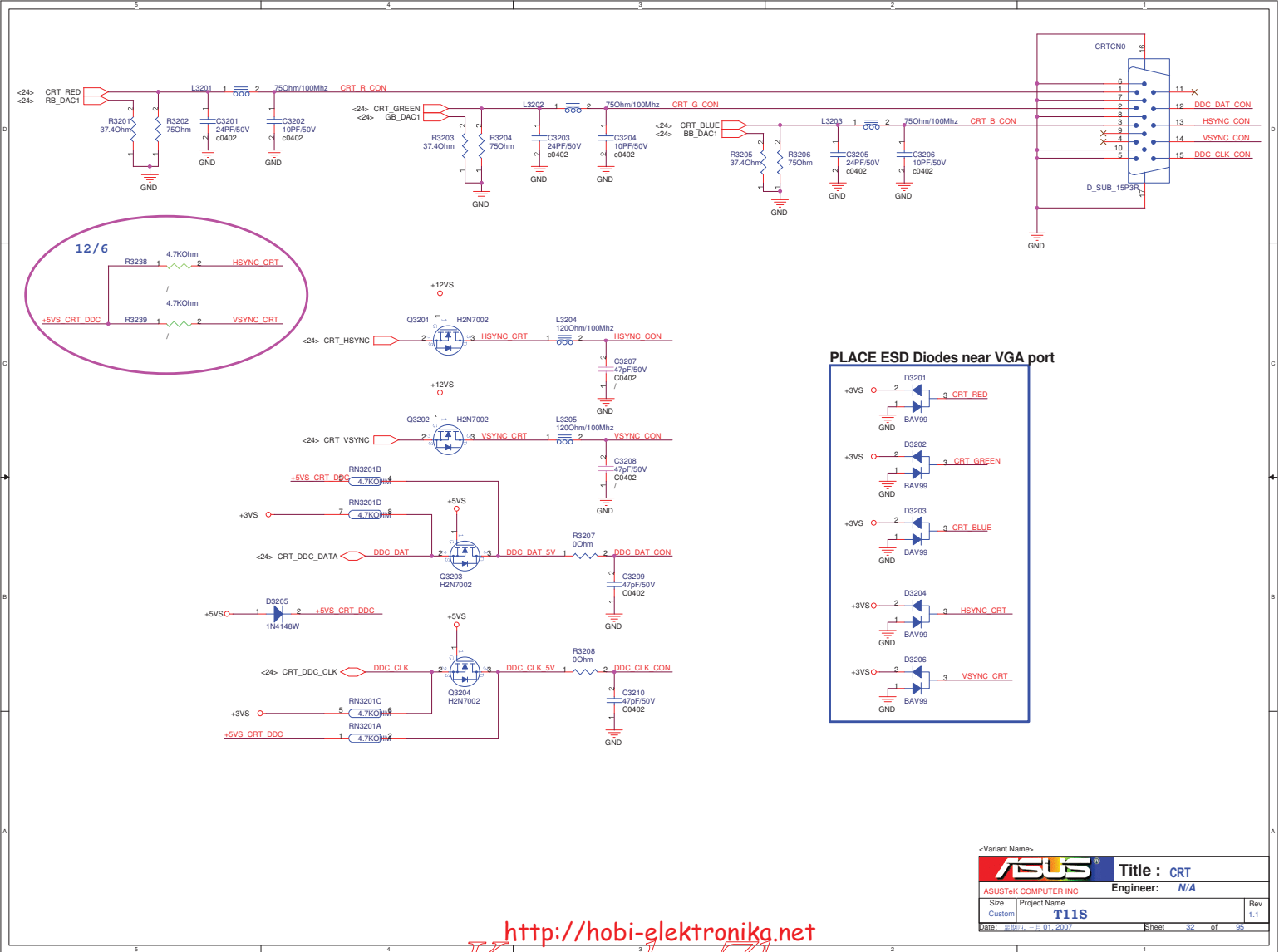
<< Kennedy_Zhang >>



<Variant Name>			
ASUS		Title : FINGER PRINT	
ASUSTeK COMPUTER INC		Engineer: N/A	
Size	Project Name		Rev
Custom	T11S		1.1
Date: 2010.01.01 01:2007		Sheet	81 of 95

<http://hobi-elektronika.net>

<< Kennedy_Zhang >>

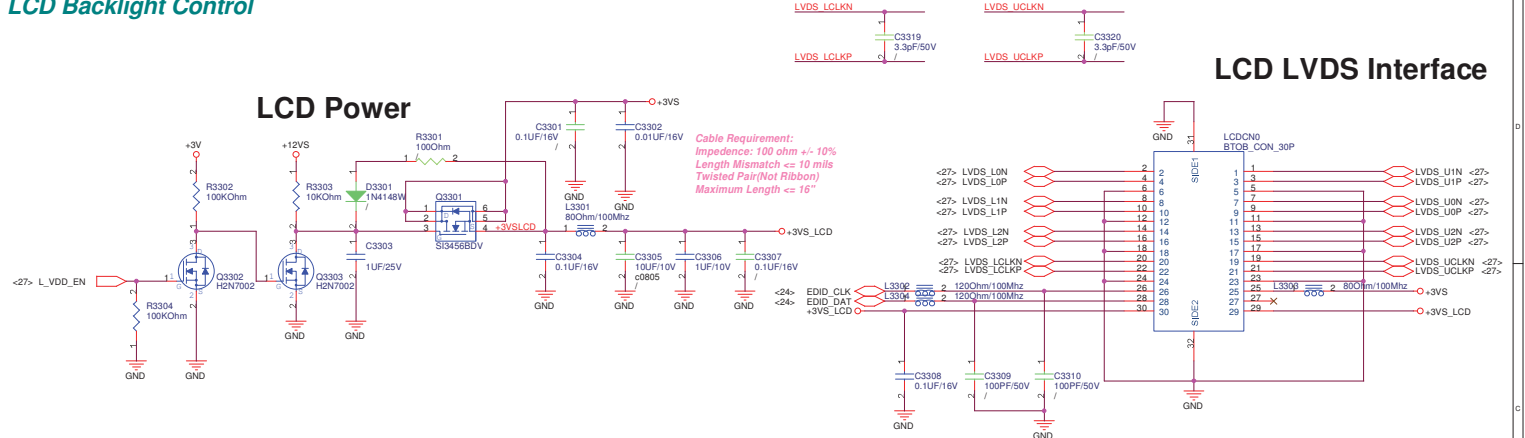


<http://hobi-elektronikg.net>

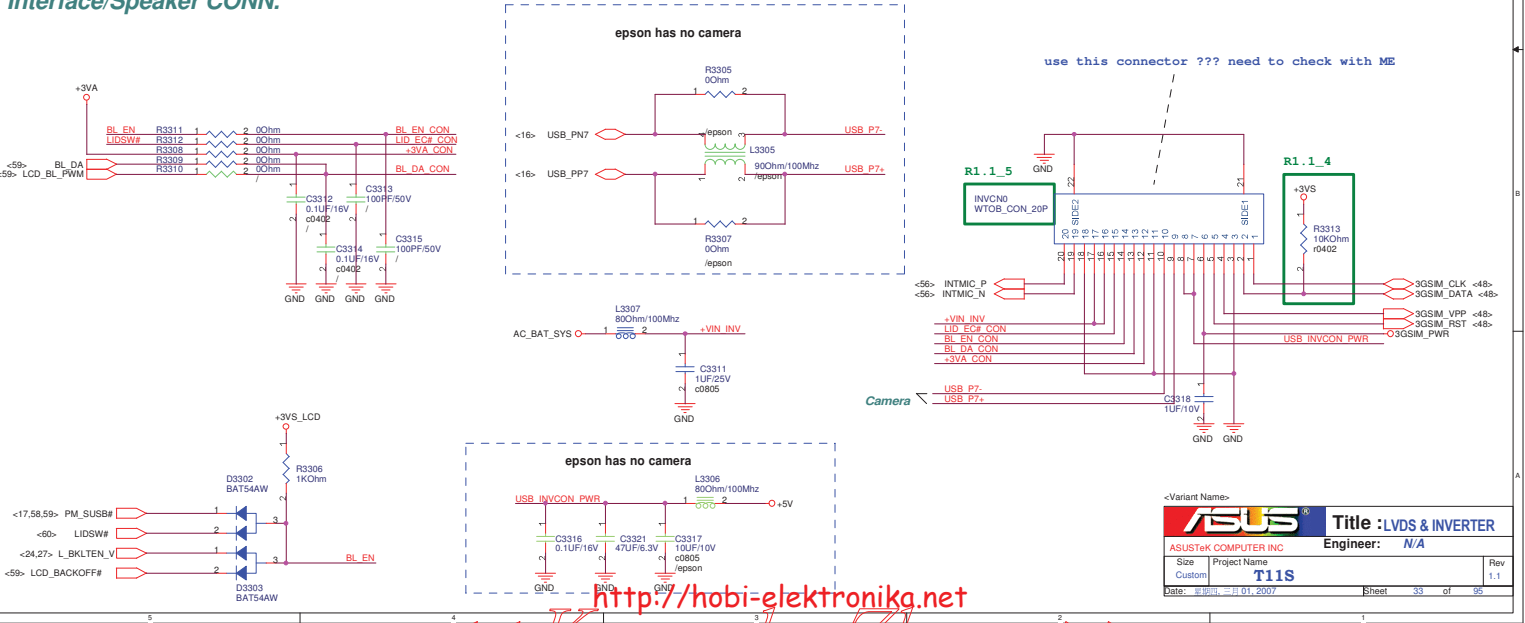
<< Kennedy_Zhang >>

<Variant Name>		Title : CRT	
ASUSTek COMPUTER INC.		Engineer: N/A	
Size	Project Name	Rev	
Custom	T11S	1.1	
Date: 2007.01.01	Sheet 32 of 95		

LCD Backlight Control

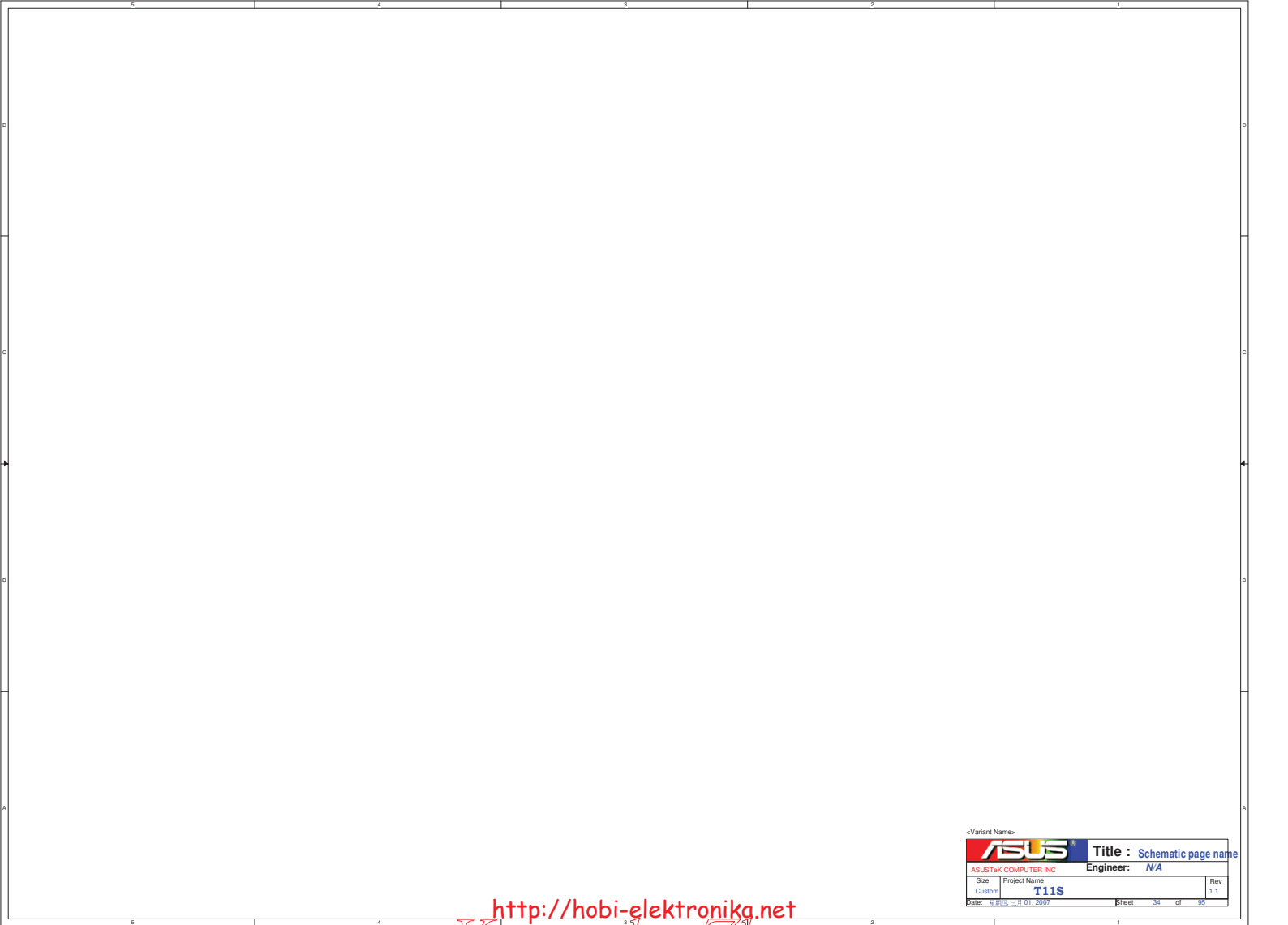


INVERTER Interface/Speaker CONN.



<http://hobi-elektronikg.net>
<< Kennedy_Zhang >>

ASUS		Title :LVDS & INVERTER	
ASUSTek COMPUTER INC.		Engineer: N/A	
Size	Project Name	Rev	
Custom	T11S	1.1	
Date: 2007.01.01	Sheet 33	of 95	



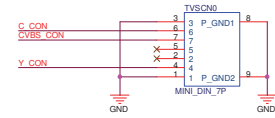
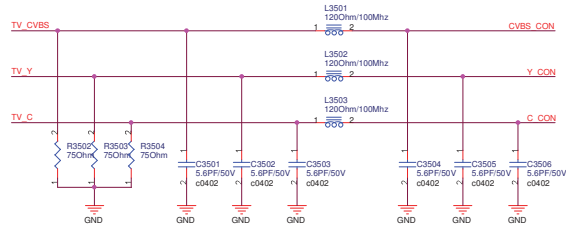
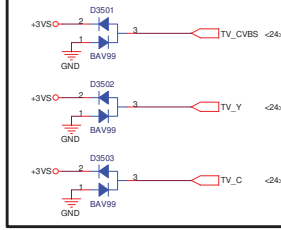
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ASUSTeK COMPUTER INC		Engineer: N/A	
Size	Project Name		Rev
Custom	T11S		1.1
Date: 2010.01.01		Sheet	34 of 35

<http://hobi-elektronika.net>

<< Kennedy_Zhang >>

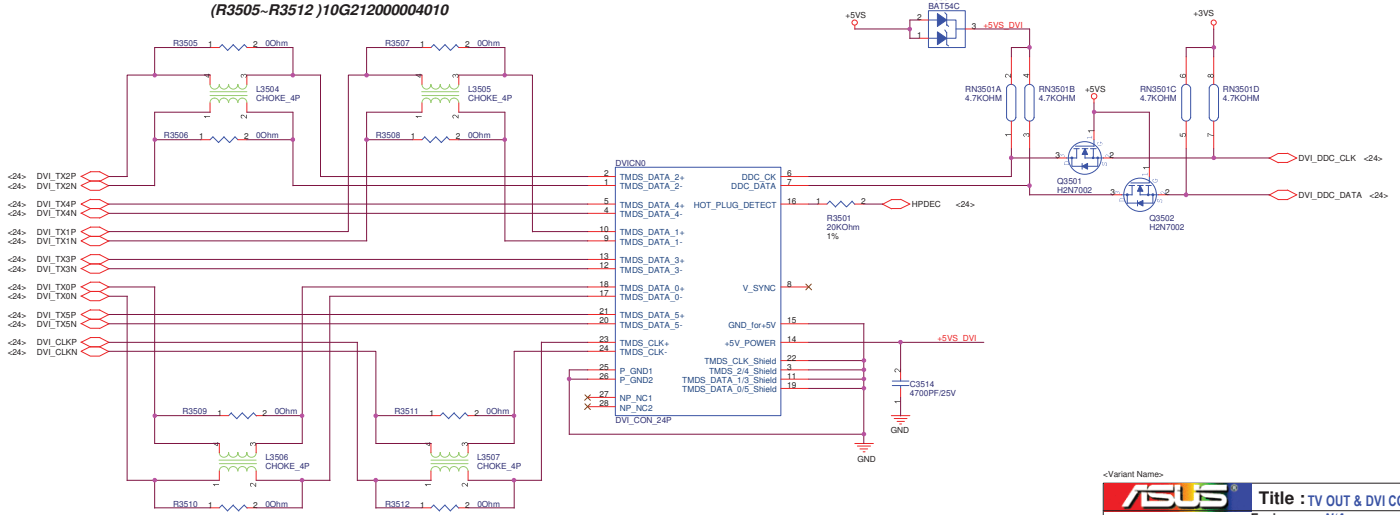
TV OUT

PLACE ESD Diodes near TV port



DVI

(R3505~R3512) 10G212000004010



Title : TV OUT & DVI CON.			
ASUSTek COMPUTER INC		Engineer: N/A	
Size	Project Name	Rev	
Custom	T11S	1.1	
Date: 2010.01.01	2007	Sheet	95 of 95

<http://hobi-elektronika.net>

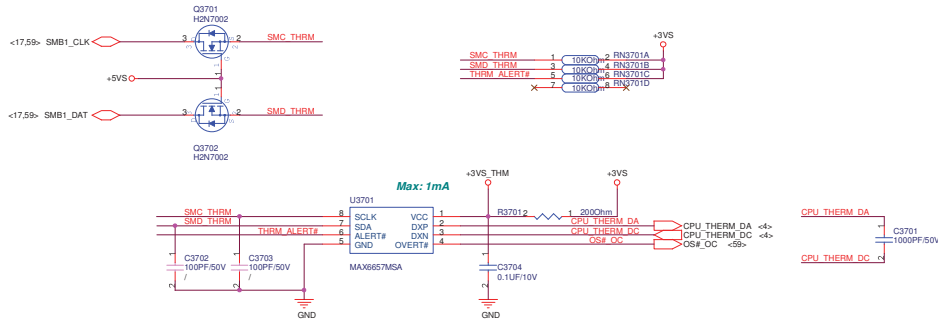
<< Kennedy_Zhang >>

<Variant Name>			Title : Schematic page name	
			Engineer: N/A	
ASUSTeK COMPUTER INC.				
Size	Project Name			Rev
Custom	T11S			1.1
Date: 2010.01.01			Sheet	95 of 95

<http://hobi-elektronika.net>

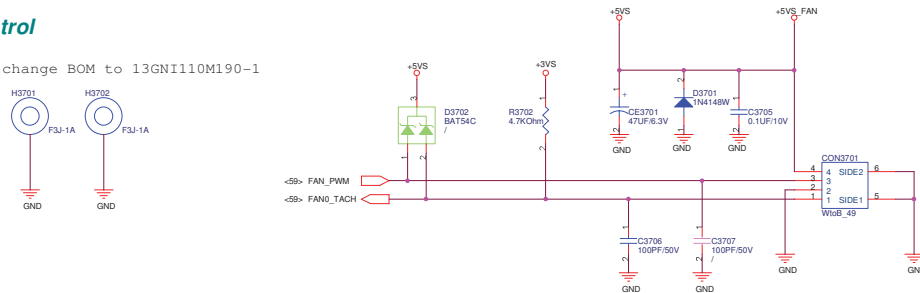
<< Kennedy_Zhang >>

Thermal Sensor



DC FAN Control

change BOM to 13GN110M190-1



<Variant Name>

ASUS		Title : THER SENSOR & FAN	
ASUSTek COMPUTER INC. NB1		Engineer: N/A	
Size	Project Name	Rev	
Custom	T11S	1.1	
Date: 2010.01.01	2007	Sheet	87 of 95

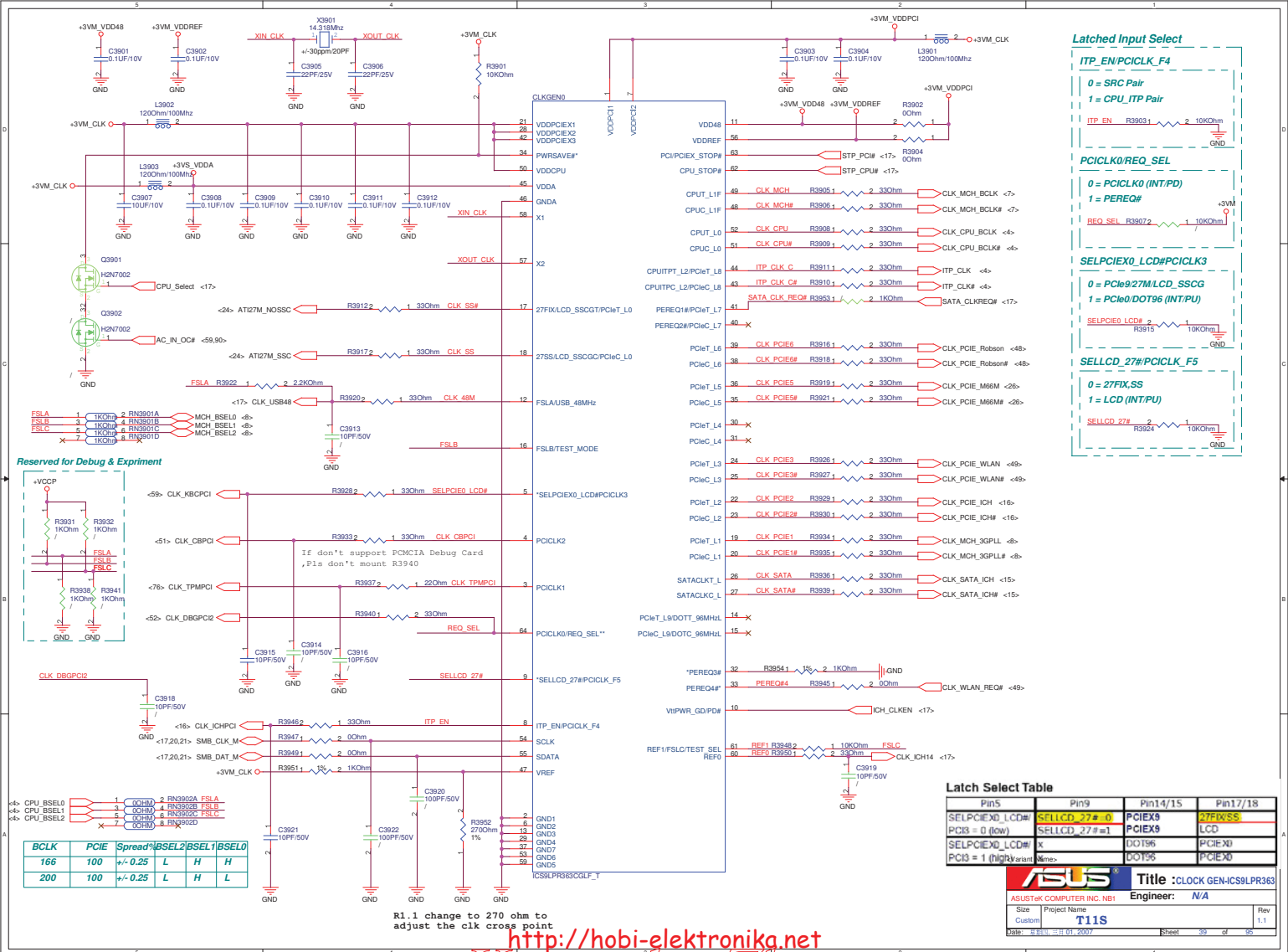
<http://hobi-elektronika.net>

<< Kennedy_Zhang >>

<Variant Name>			Title : Schematic page name	
			Engineer: N/A	
ASUSTeK COMPUTER INC.				
Size	Project Name			Rev
Custom	T11S			1.1
Date: 2010.01.01			Sheet	38 of 95

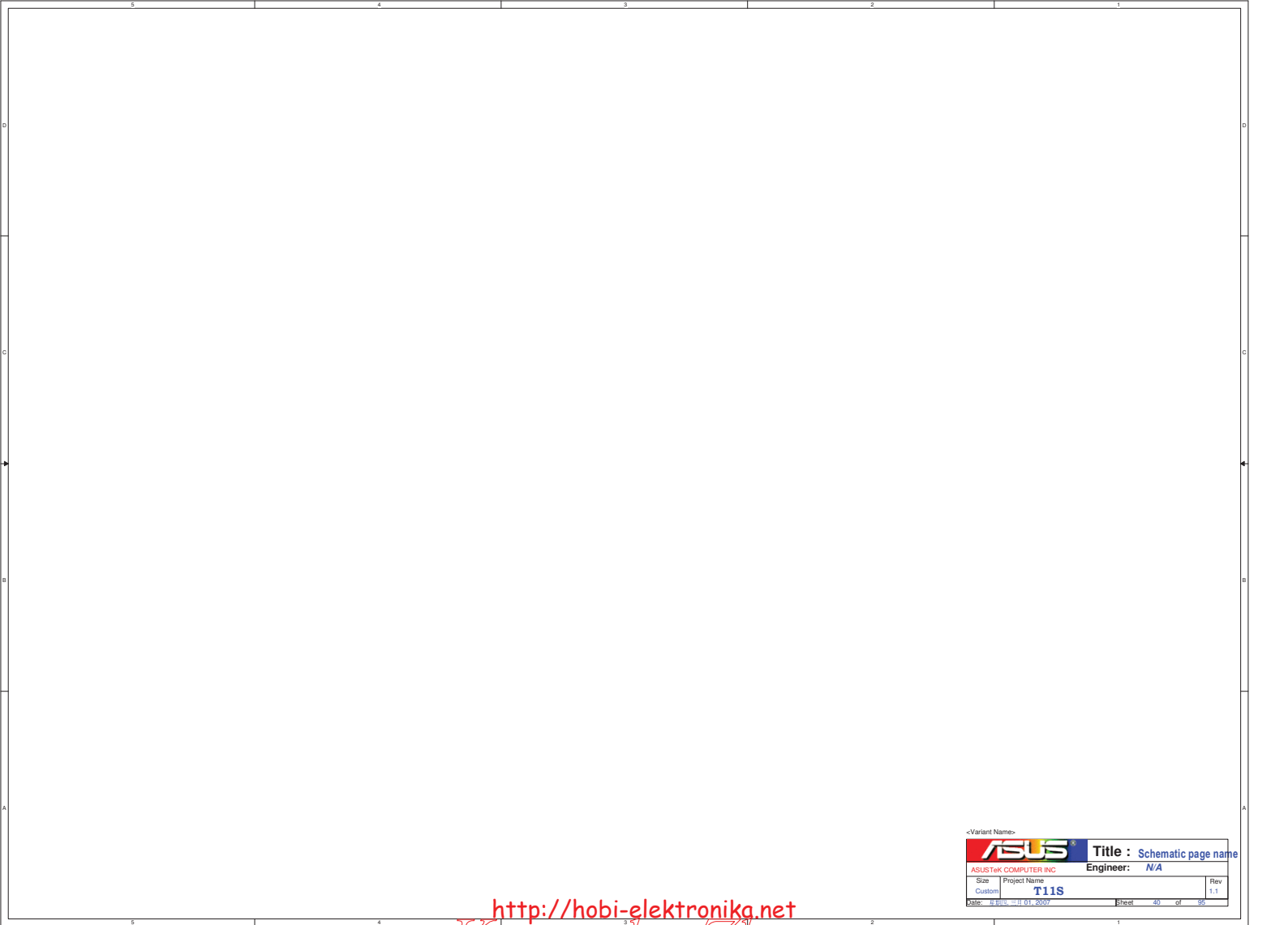
<http://hobi-elektronika.net>

<< Kennedy_Zhang >>



adjust the clk cross point

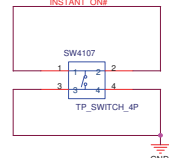
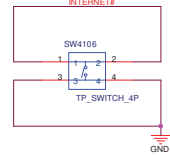
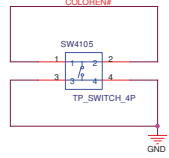
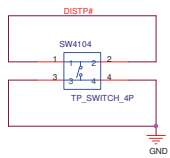
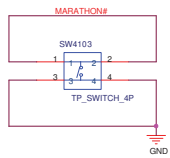
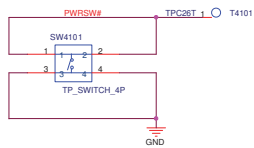
<< Kennedy_Zhang >>



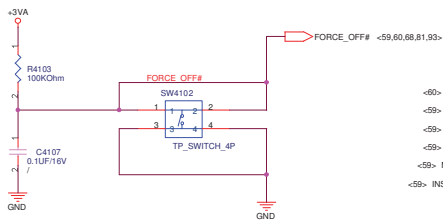
<http://hobi-elektronika.net>

<< Kennedy_Zhang >>

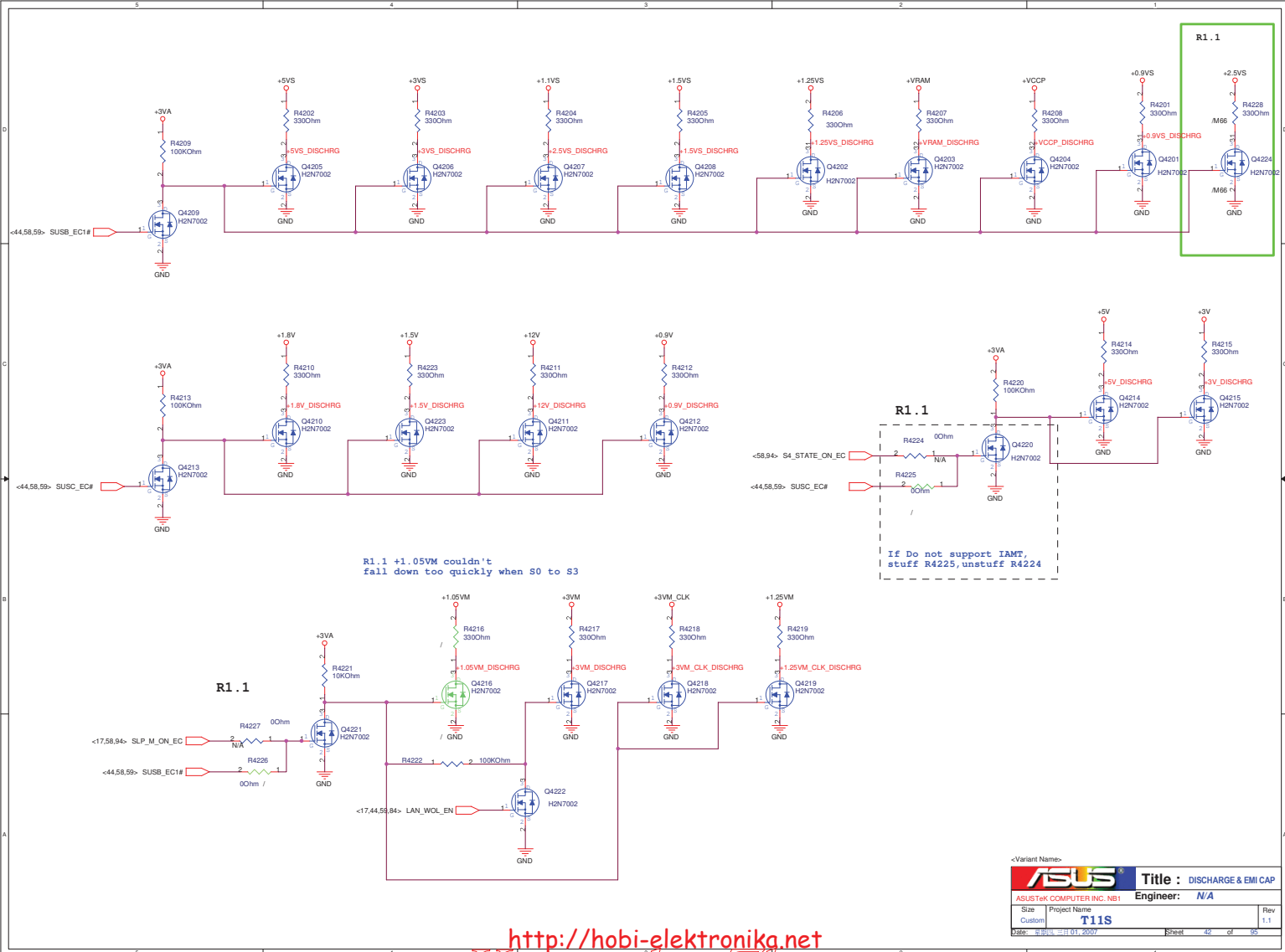
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		Title : Schematic page name	
ASUSTeK COMPUTER INC		Engineer: N/A	
Size	Project Name		Rev
Custom	T11S		1.1
Date: 2010.01.01		Sheet 40	of 95



SHUT_DOWN#

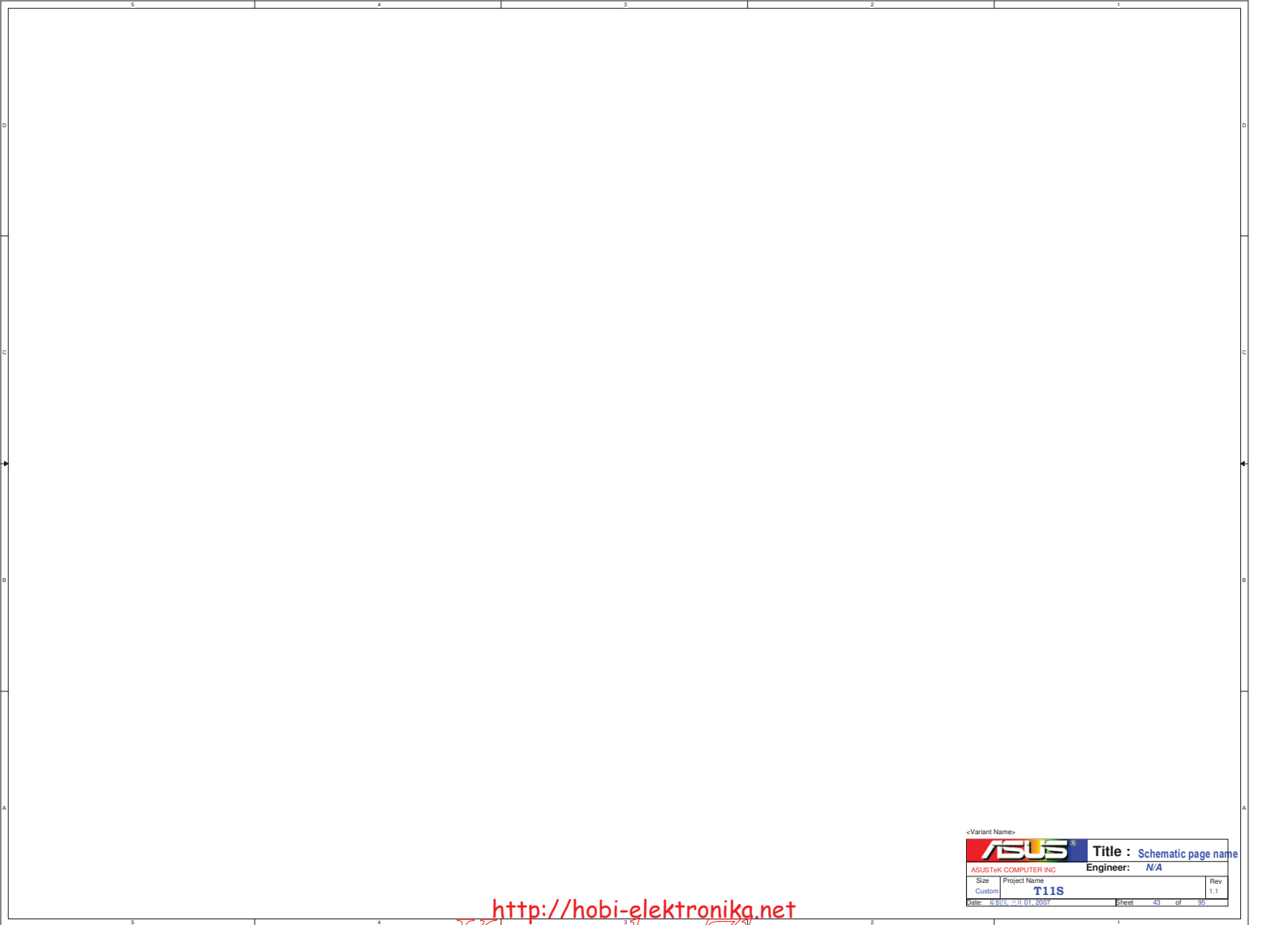


<60>	PWRSW#	C4103	1	2	0.1uF/16V
<59>	DISTP#	C4104	1	2	0.1uF/16V
<58>	COLOREN#	C4105	1	2	0.1uF/16V
<57>	INTERNET#	C4106	1	2	0.1uF/16V
<56>	MARATHON#	C4108	1	2	0.1uF/16V
<55>	INSTANT_ON#	C4109	1	2	0.1uF/16V



<http://hobi-elektronika.net>

<< Kennedy_Zhang >>



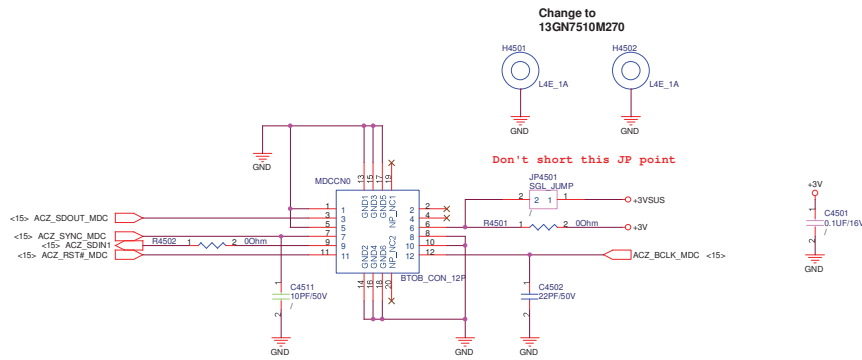
<http://hobi-elektronika.net>

<< Kennedy_Zhang >>

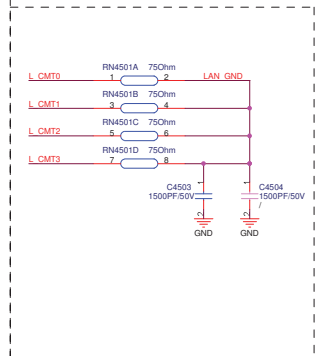
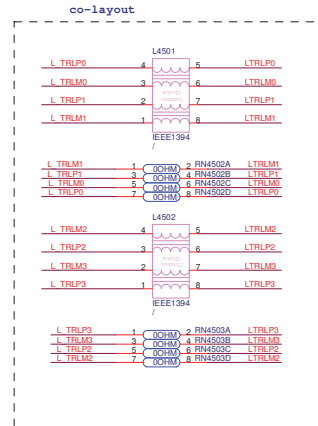
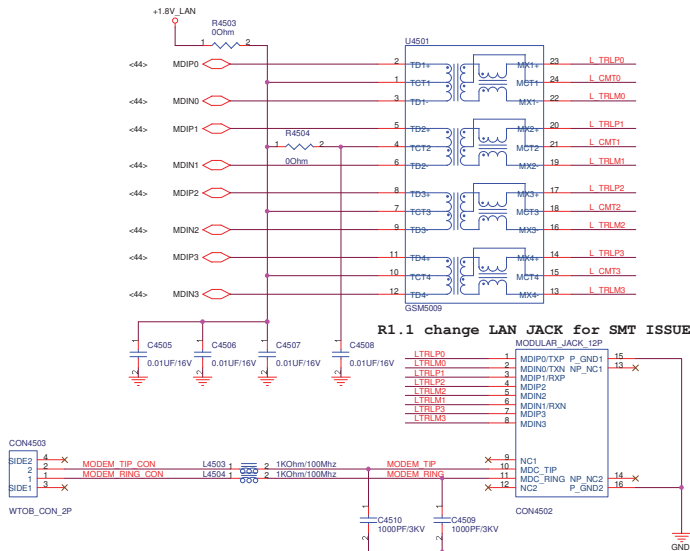
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ASUSTeK COMPUTER INC		Engineer: N/A	
Size	Project Name		Rev
Custom	T11S		1.1
Date: 2010.01.01		Sheet 49	of 95

<Variant Name>		 Title : LAN-L1	
ASUSTeK COMPUTER INC. NB1		Engineer: N/A	
Size Custom	Project Name T11S		Rev 1.1
Date: 2007年01月01日		Sheet	44 of 95

MDC CONN.



LAN CONN.

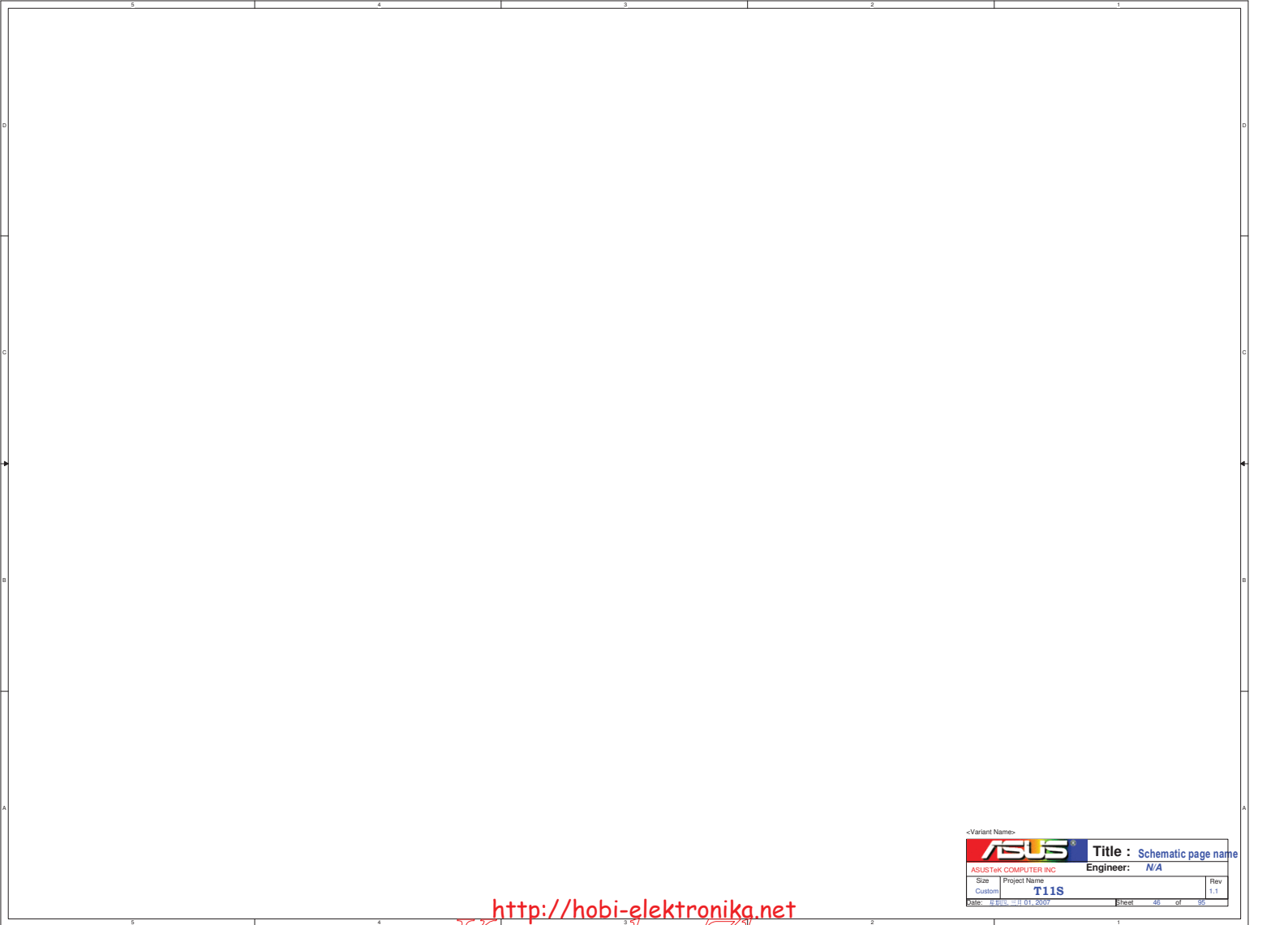


<Variant Name>

ASUS		Title : MDC & RJ45+11	
ASUSTek COMPUTER INC. NB1		Engineer: N/A	
Size	Project Name	Rev	
Custom	T11S	1.1	
Date: 2010.01.01	01.01.2007	Sheet	45 of 95

<http://hobi-elektronika.net>

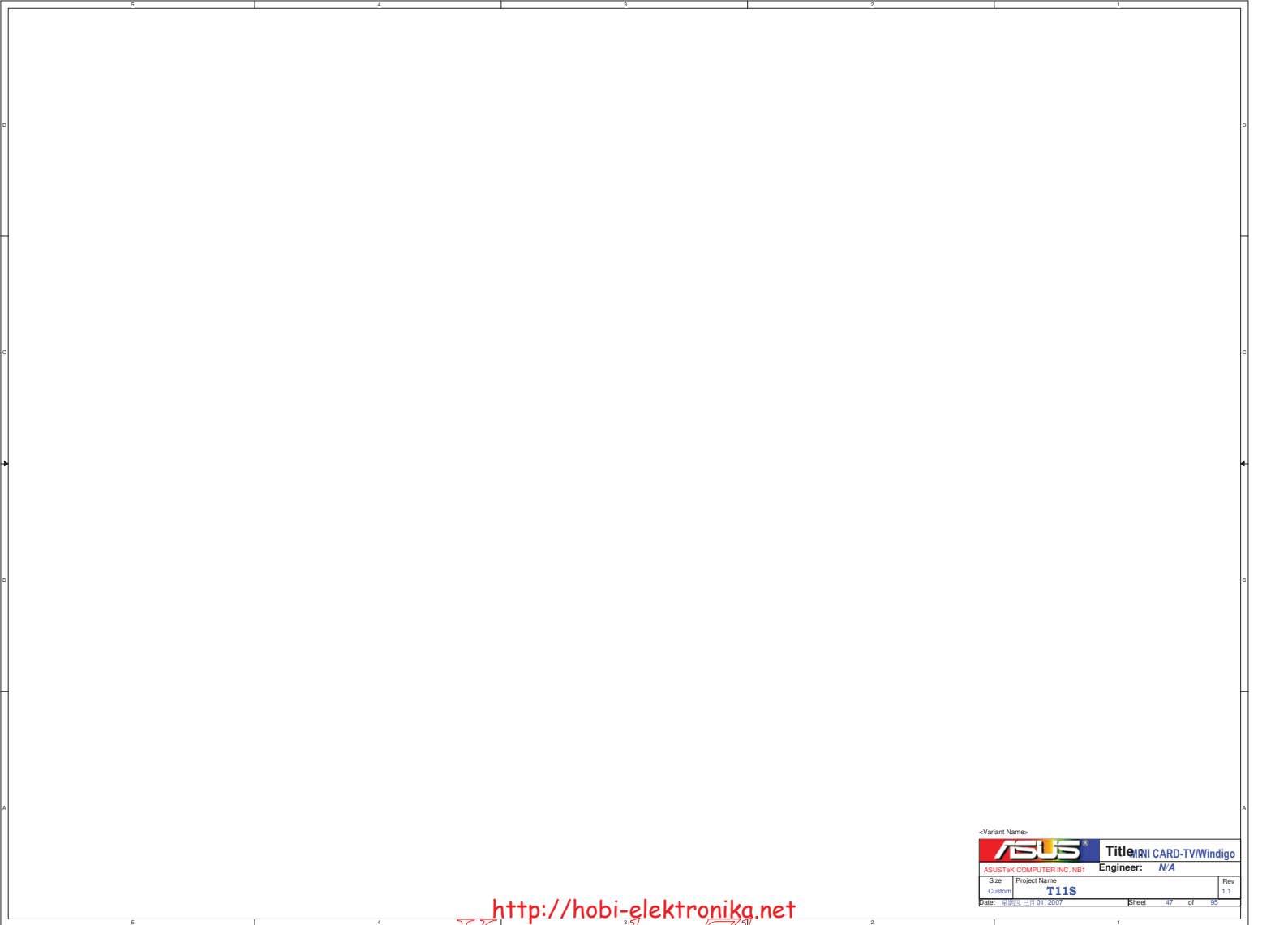
<< Kennedy_Zhang >>



<Variant Name>			
		Title : Schematic page name	
ASUSTeK COMPUTER INC		Engineer: N/A	
Size	Project Name		Rev
Custom	T11S		1.1
Date: 2010.01.01		Sheet	46 of 95

<http://hobi-elektronika.net>

<< Kennedy_Zhang >>

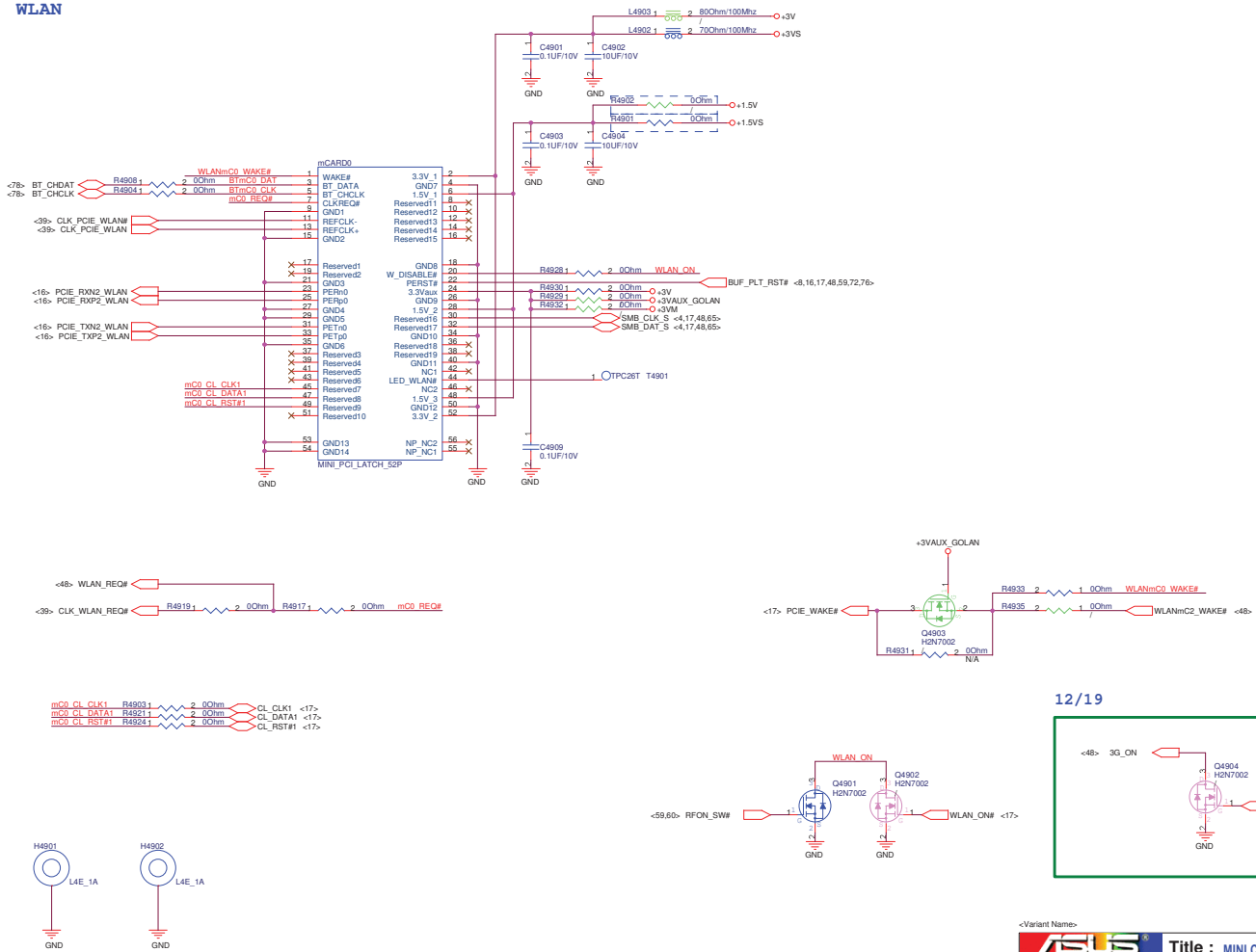


<http://hobi-elektronika.net>

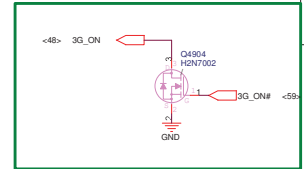
<< Kennedy_Zhang >>

<Variant Name>			
		Title: MINI CARD-TV/Windows	
ASUSTek COMPUTER INC. NB1		Engineer: N/A	
Size	Project Name		Rev
Custom	T11S		1.1
Date: 2010.11.01 01:2007		Sheet	47 of 95

1. Kedron WLAN

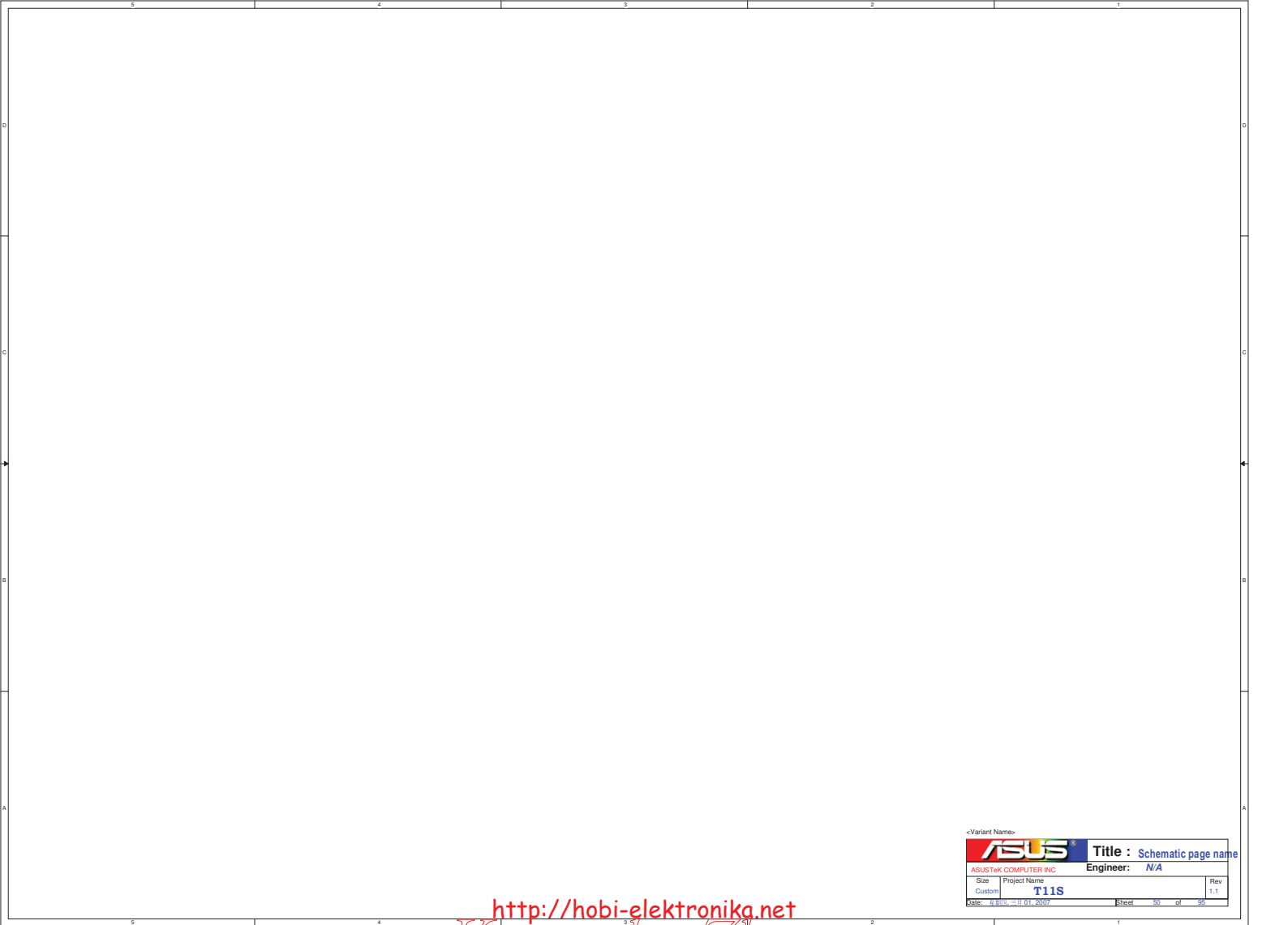


12/19



Title : MINI CARD-Kedron			
ASUSTek COMPUTER INC. NB1		Engineer: N/A	
Size	Project Name	Rev	
Custom	T11S	1.1	
Date: 2010.01.01	Sheet	49	of 95

<http://hobi-elektronika.net>
 << Kennedy_Zhang >>

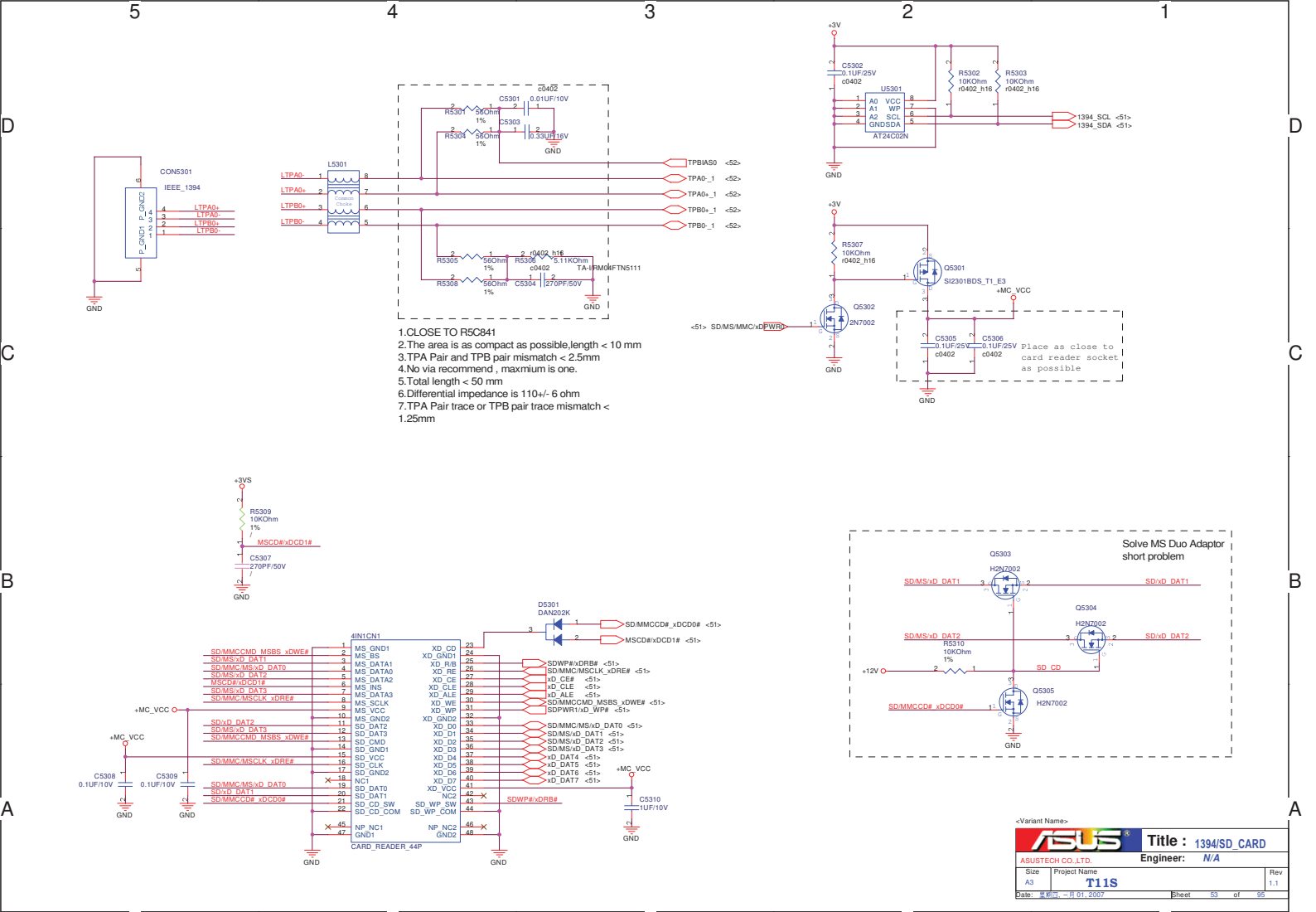


<http://hobi-elektronika.net>

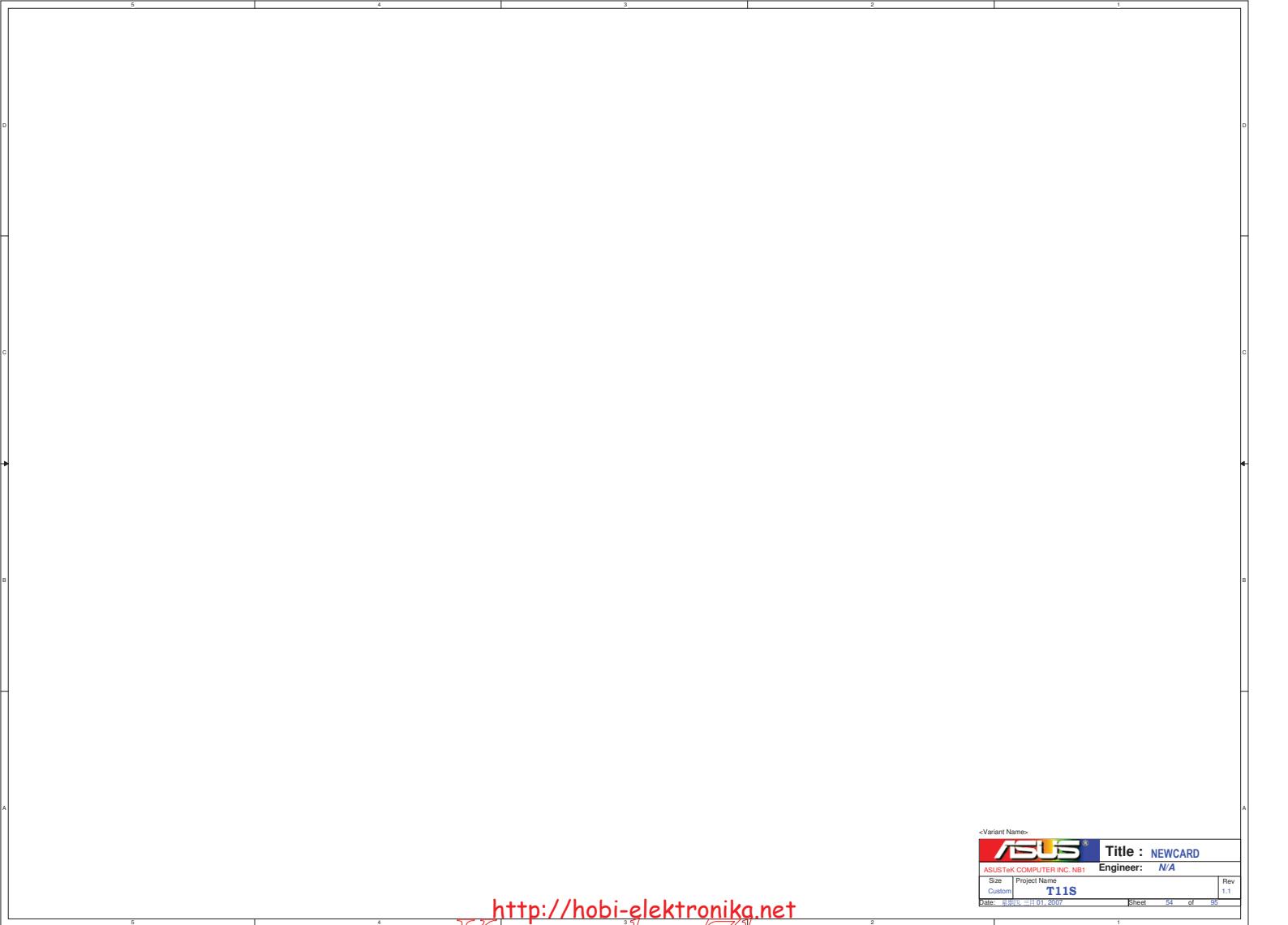
<< Kennedy_Zhang >>

<Variant Name>			
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ASUSTeK COMPUTER INC		Engineer: N/A	
Size	Project Name		Rev
Custom	T11S		1.1
Date: 2010.01.01		Sheet	50 of 95





<http://hobi-elektronika.net>
 << Kennedy_Zhang >>

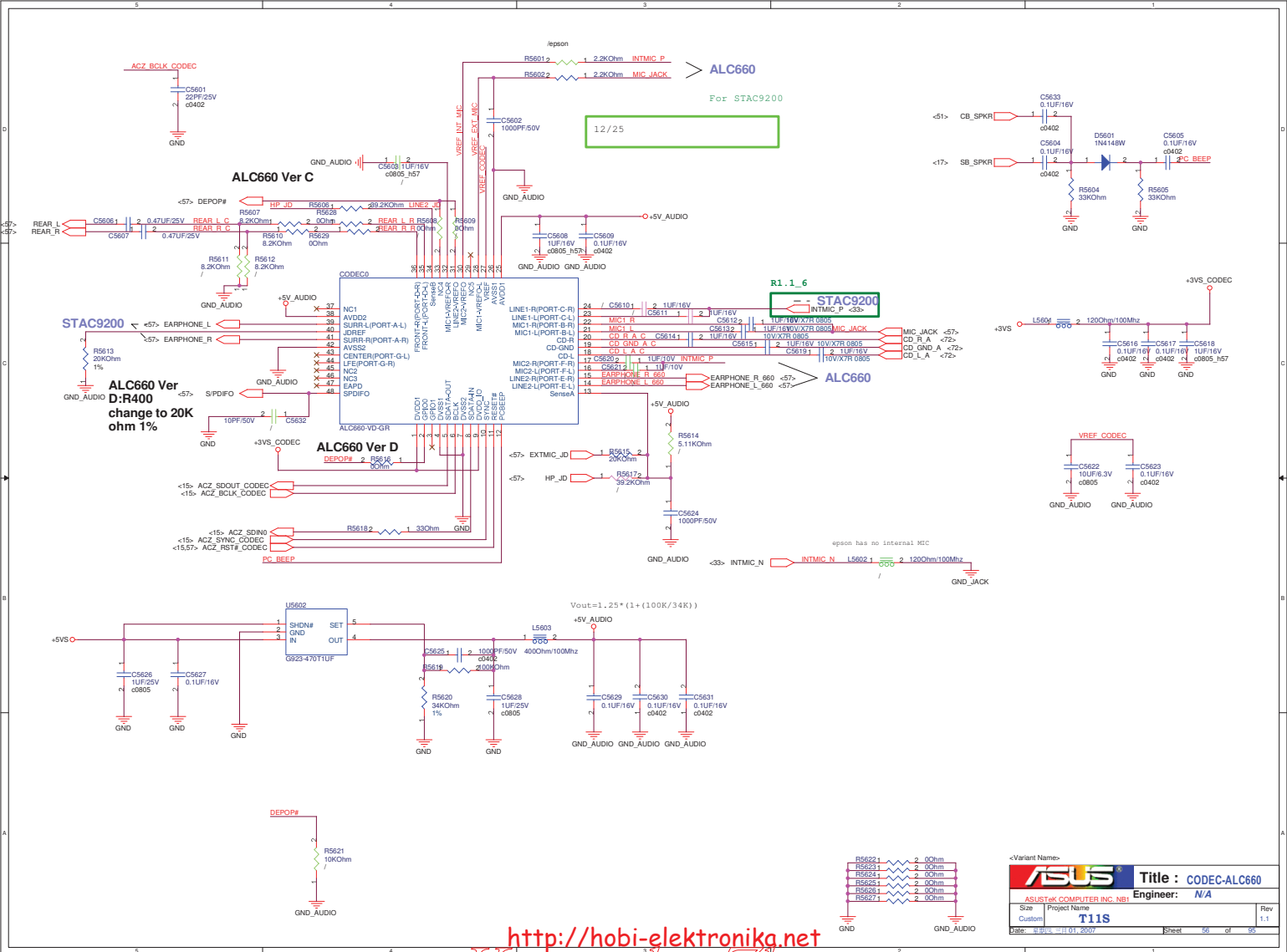


<http://hobi-elektronika.net>

<< Kennedy_Zhang >>

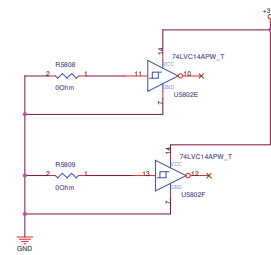
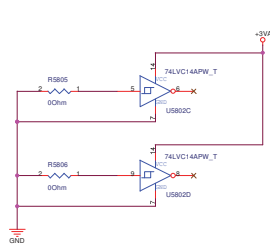
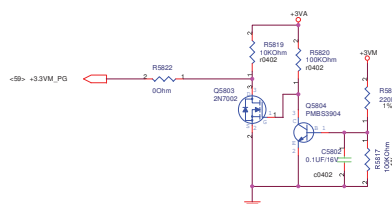
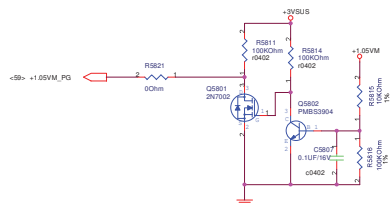
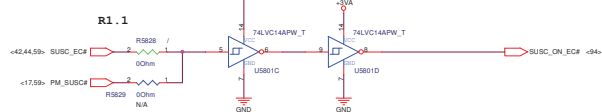
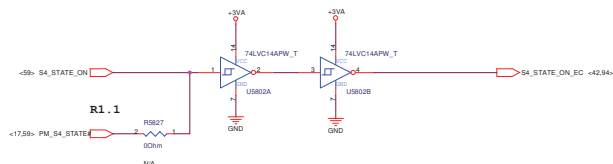
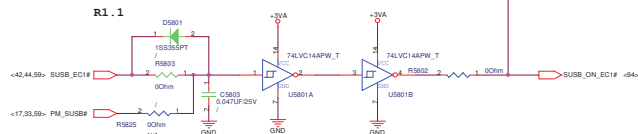
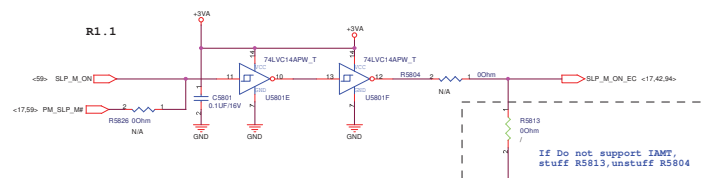
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		Title : NEWCARD	
ASUSTek COMPUTER INC. NB1		Engineer: N/A	
Size	Project Name		Rev
Custom	T11S		1.1
Date: 2010.11.01 01:2007		Sheet	54 of 95

		Title : None	
ASUSTek COMPUTER INC		Engineer: N/A	
Size Custom	Project Name T11S	Rev 1.1	
Date: 2007. 01. 01		Sheet 55	of 95



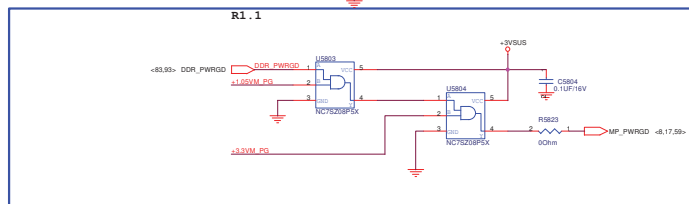
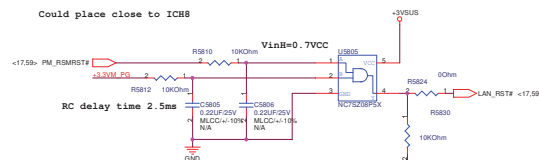
<http://hobi-elektronika.net>

R1.1 for support AMT sequence, modify the sequence by H/W



R1.1

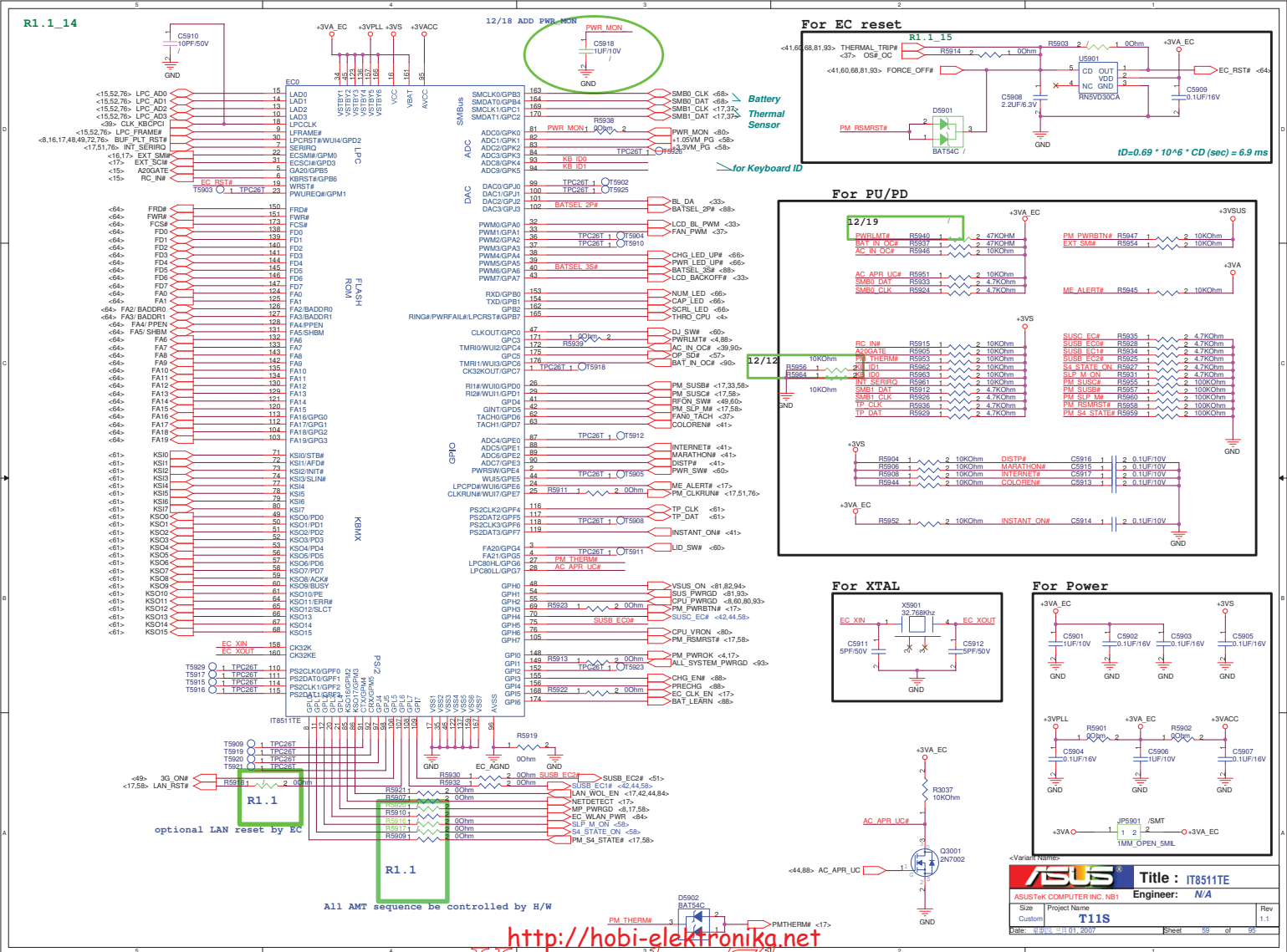
Could place close to IC88



<http://hobi-elektronika.net>

<< Kennedy_Zhang >>

ASUS		Title: Sequence Control Logic	
ASUSTeK COMPUTER INC.		Engineer: N/A	
Size	Project Name	Rev	
C	T11S	1.1	
Date: 11/11/2007		E: 11/11/2007	

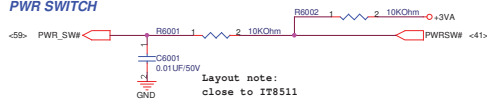


<http://hobi-elektronika.net>

<< Kennedy_Zhang >>

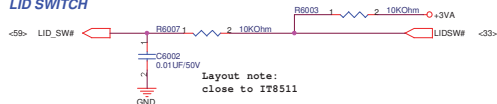
For Switch

PWR SWITCH



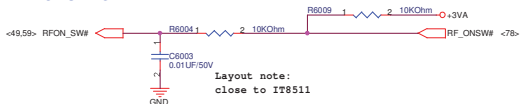
Layout note:
close to I78511

LID SWITCH



Layout note:
close to I78511

RF ON SWITCH



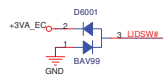
Layout note:
close to I78511



If you don't use Audio DJ
function, please use this
schematics

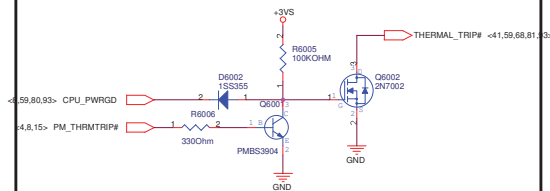
Note:

This LID_EC# is a signal from
inverter board, it is easy to
cause high voltage damage when
plugging inverter board connector
to M/B with AC present. It needed
to add bidirectional diode to
protect this pin.



Layout note:
close to connector

For Thermal Control Method



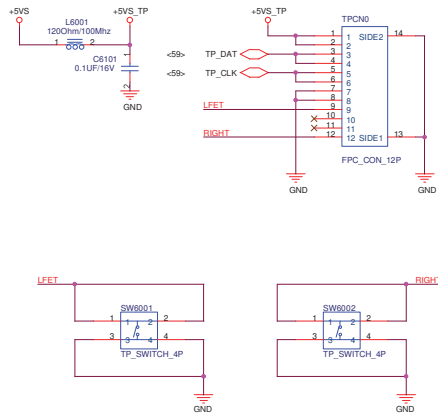
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ASUS		Title : Schematic page name	
ASUSTeK COMPUTER INC		Engineer: N/A	
Size	Project Name	Rev	
Custom	T11S	1.1	
Date: 15/03/2007		Sheet	60 of 95

<http://hobi-elektronika.net>

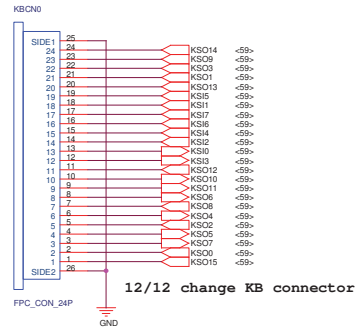
<< Kennedy_Zhang >>

For Touch-Pad



For Keyboard

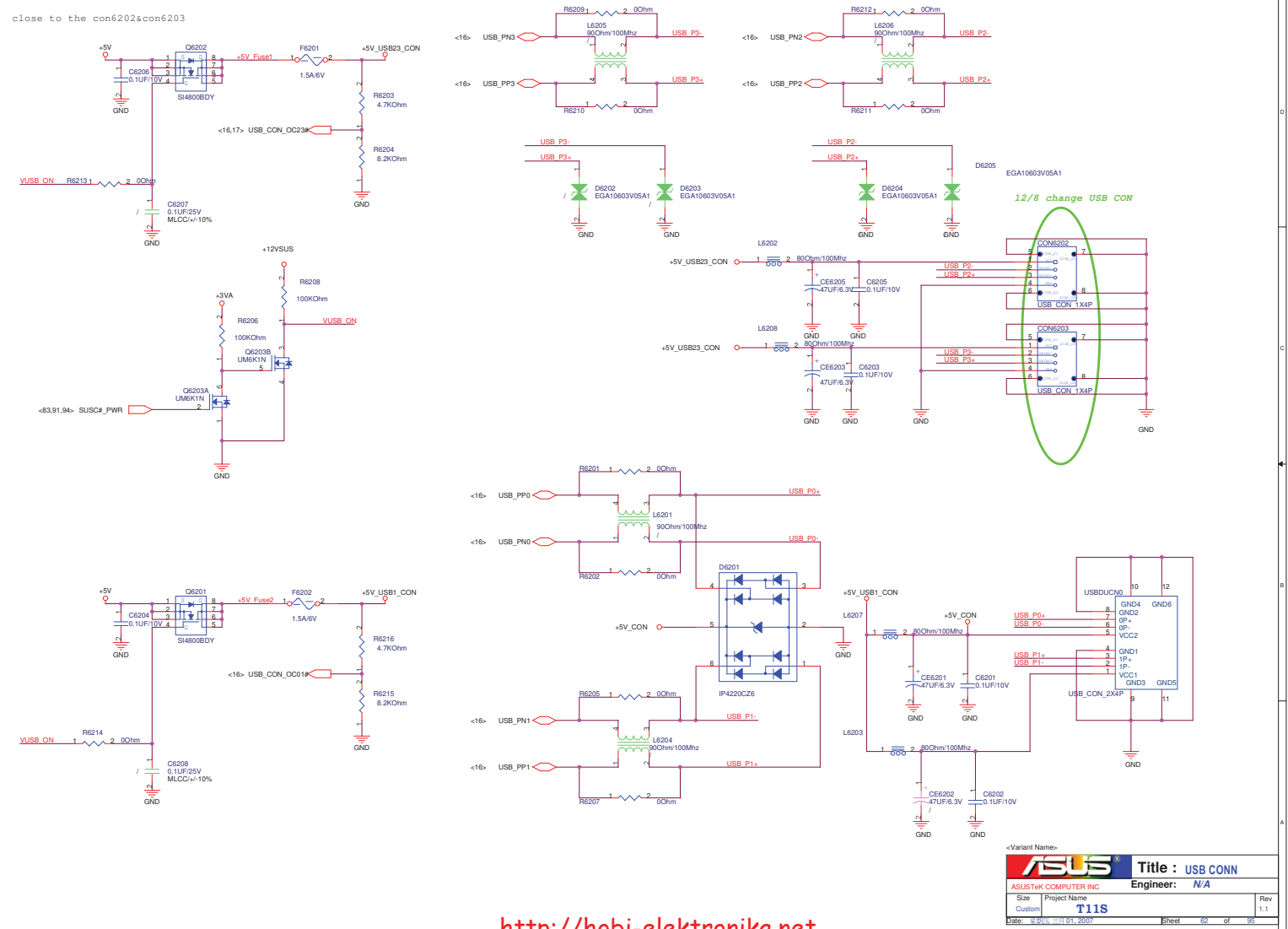
R1.1 modify the pin connect



<Variant Name>			
ASUS		Title : Touch Pad & KB	
ASUSTeK COMPUTER INC. NB1		Engineer: N/A	
Size	Project Name		Rev
Custom	T11S		1.1
Date: 2013.11.01	2013.11.01	Sheet	61 of 95

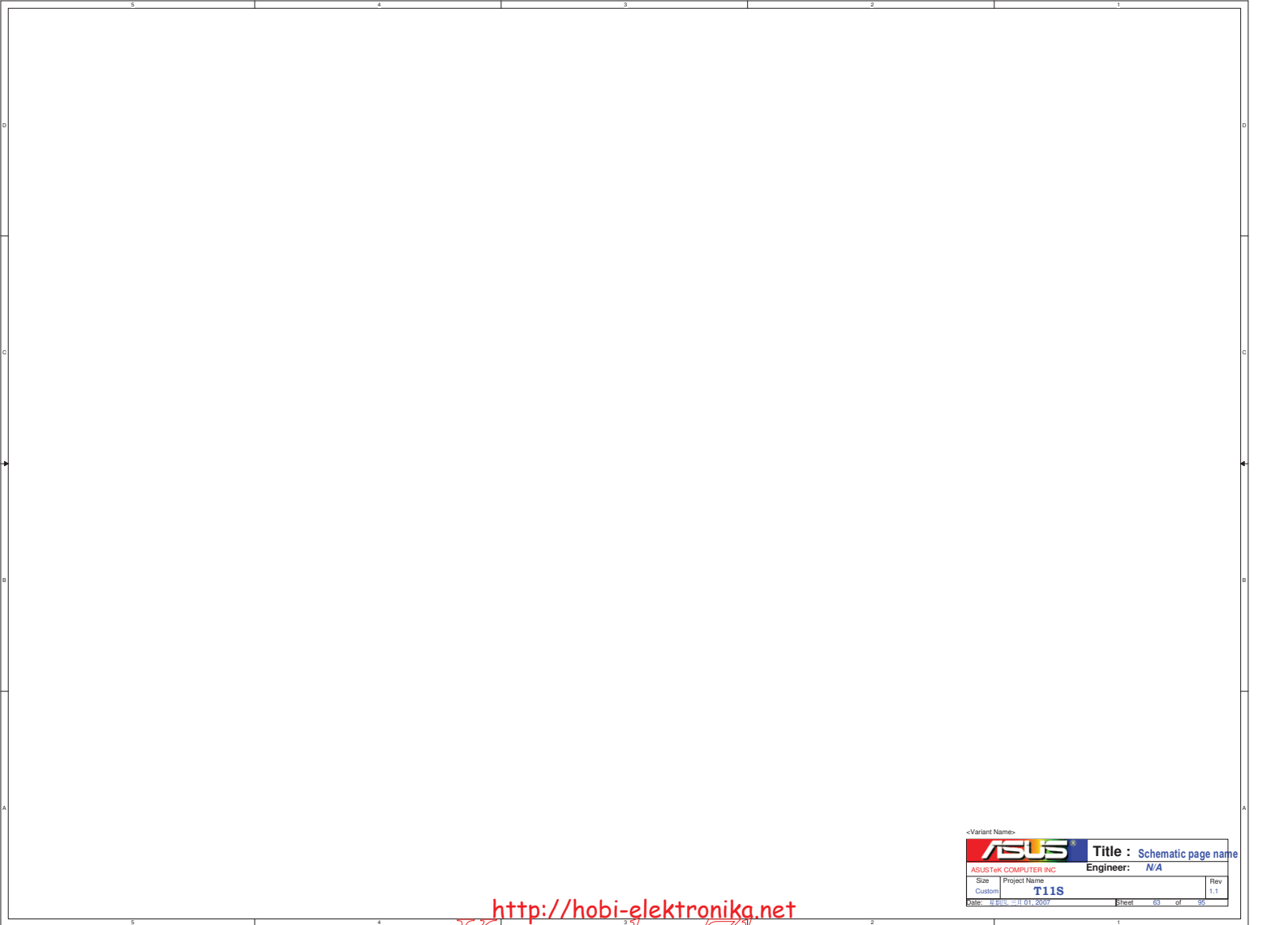
<http://hobi-elektronika.net>

<< Kennedy_Zhang >>



<http://hobi-elektronika.net>

<< Kennedy_Zhang >>



<http://hobi-elektronika.net>

<< Kennedy_Zhang >>

<Variant Name>			
		Title : Schematic page name	
ASUSTeK COMPUTER INC		Engineer: N/A	
Size	Project Name		Rev
Custom	T11S		1.1
Date: 2010.01.01		Sheet 69	of 95

FA2/ BADDR0 & FA3/ BADDR1

00: PNPNCNG Access Register Pair Are 002Eh and 002Fh
 10: PNPNCNG Access Register Pair Are 004Eh and 004Fh
 01: PNPNCNG Access Register Pair Are Determined by EC Domain Registers SWCBALR and SWCBAHR.
 11: Reserved

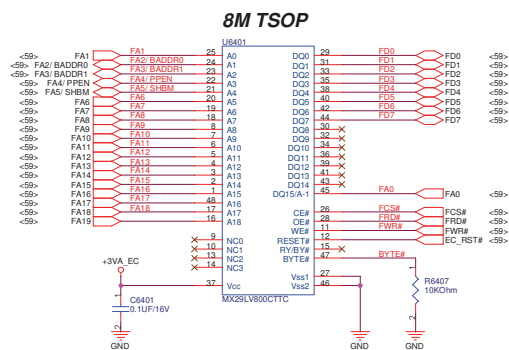
FA0/ PPEN

0: Normal
 1: KBS Interface Pins are Switched to Parallel Port Interface for In-System Programming

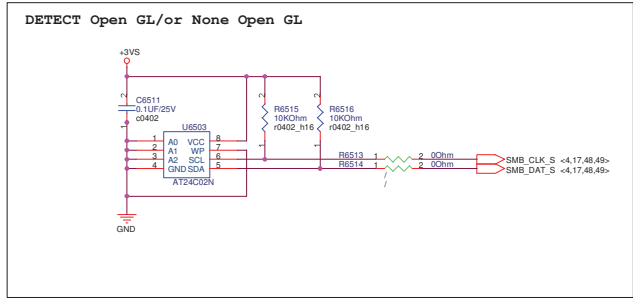
FA5/ SHBM

0: Disable Shared Memory with Host BIOS
 1: Enable Shared Memory with Host BIOS

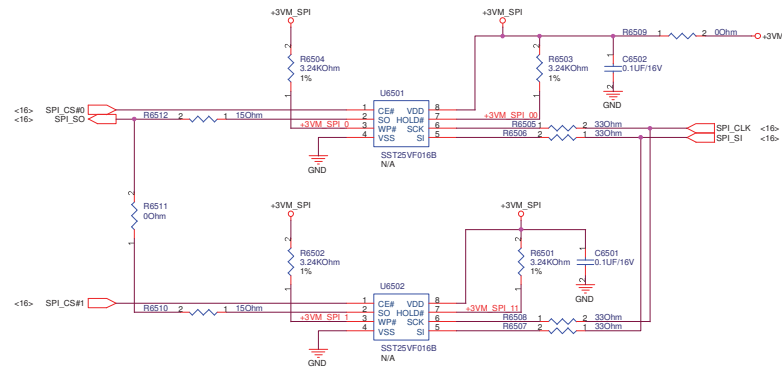
Note: Sampled at VSTBY Power Up Reset



		Title : ISA ROM	
ASUSTeK COMPUTER INC. NBI		Engineer: N/A	
Size Custom	Project Name T11S	Rev 1.1	
Date: 2007.01.01		Sheet 64	of 95



**1.1 1/24
change to 16Mbit SPI flash**

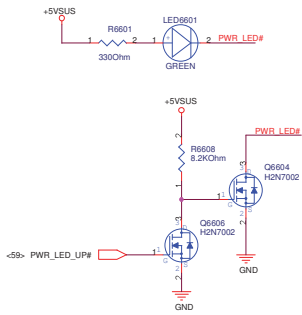


<Variant Name>		ASUS Title :SPI ROM	
ASUSTek COMPUTER INC. NB1		Engineer: N/A	
Size	Project Name	Rev	
Custom	T11S	1.1	
Date: 2010.01.01	2007	Sheet	65 of 95

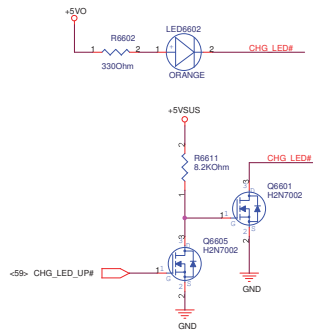
<http://hobi-elektronika.net>

<< Kennedy_Zhang >>

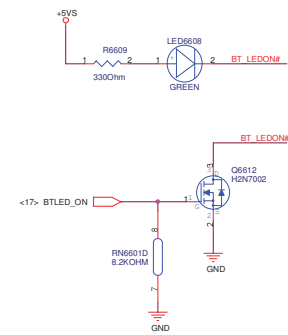
For Power LED



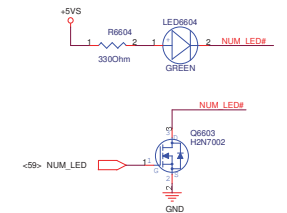
For Battery LED



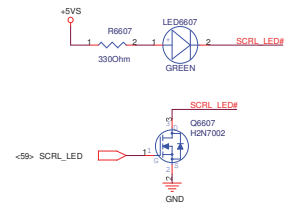
For BT LED



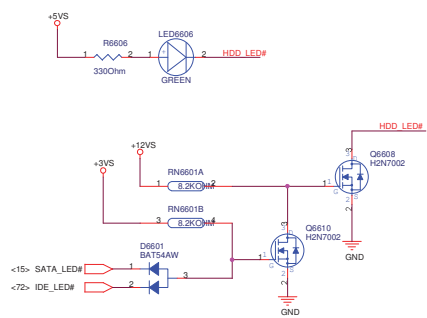
For Number Lock



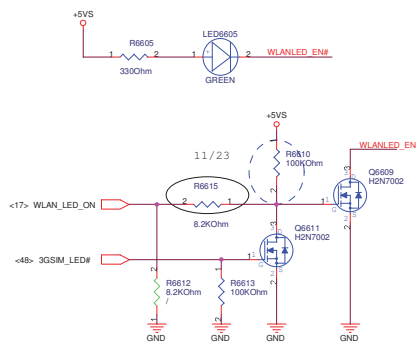
For Scroll Lock



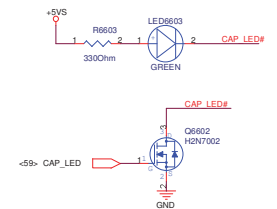
For SATA/IDE LED



For WireLess LED



For Caps. Lock

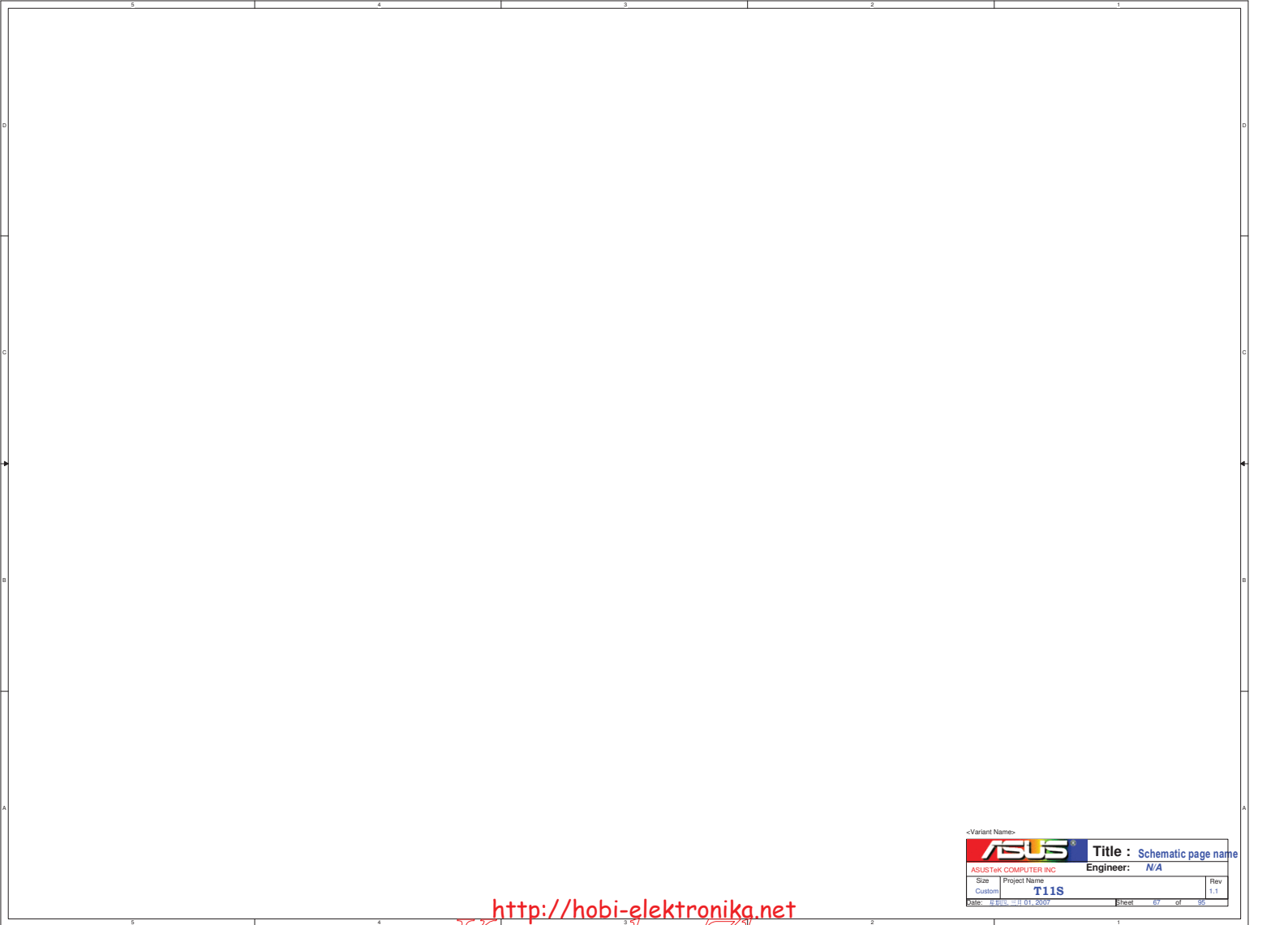


<Variant Name>

ASUS		Title : LED	
ASUSTek COMPUTER INC		Engineer: N/A	
Size	Project Name	Rev	
Custom	T11S	1.1	
Date: 2010.01.01	Sheet	66	of 95

<http://hobi-elektronika.net>

<< Kennedy_Zhang >>



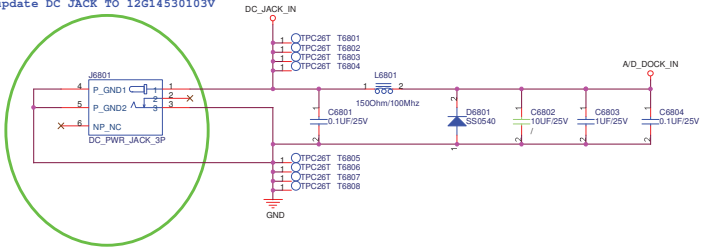
<http://hobi-elektronika.net>

<< Kennedy_Zhang >>

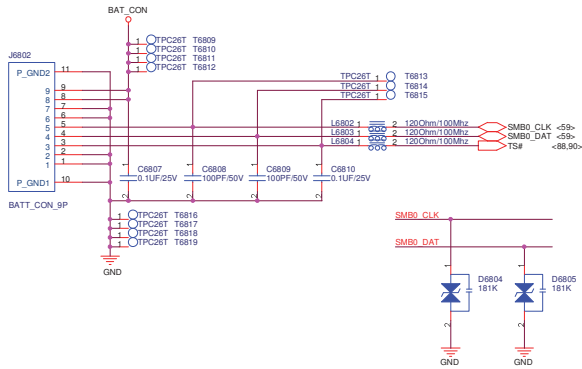
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		Title : Schematic page name	
ASUSTeK COMPUTER INC		Engineer: N/A	
Size	Project Name		Rev
Custom	T11S		1.1
Date: 2010.01.01		Sheet	67 of 95

DC IN

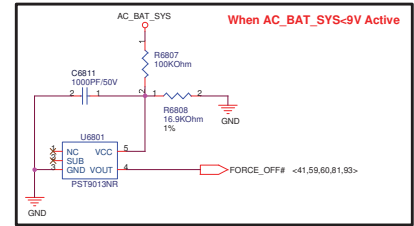
12/14 update DC JACK TO 12G14530103V



BAT IN



Without Battery & Pull out Adapter

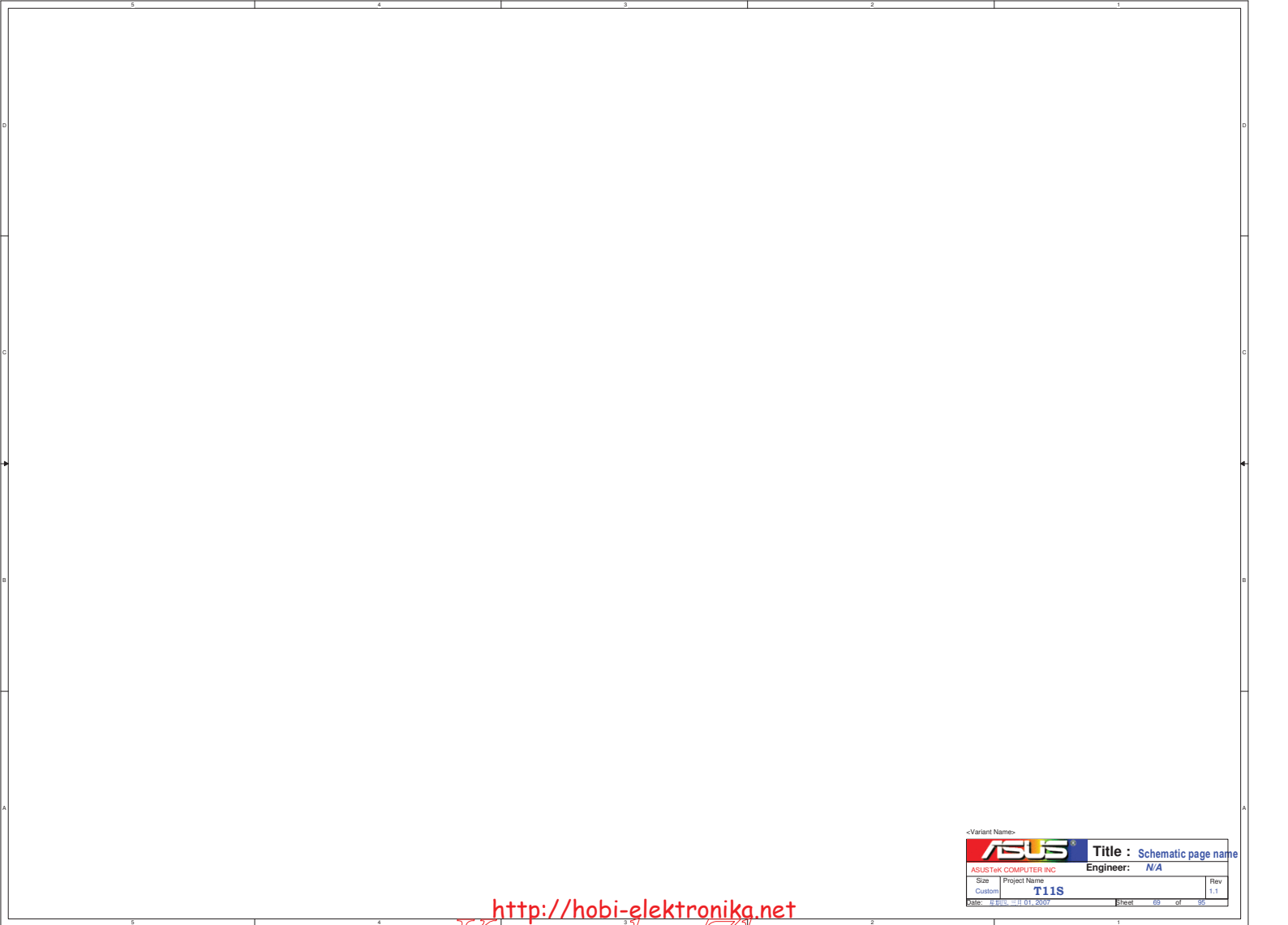


<Variant Name>

ASUS		Title : DC & BAT IN	
ASUSTek COMPUTER INC. NB1		Engineer: N/A	
Size	Project Name	Rev	
Custom	T11S	1.1	
Date: 2010.01.01	2007	Sheet	66 of 95

<http://hobi-elektronika.net>

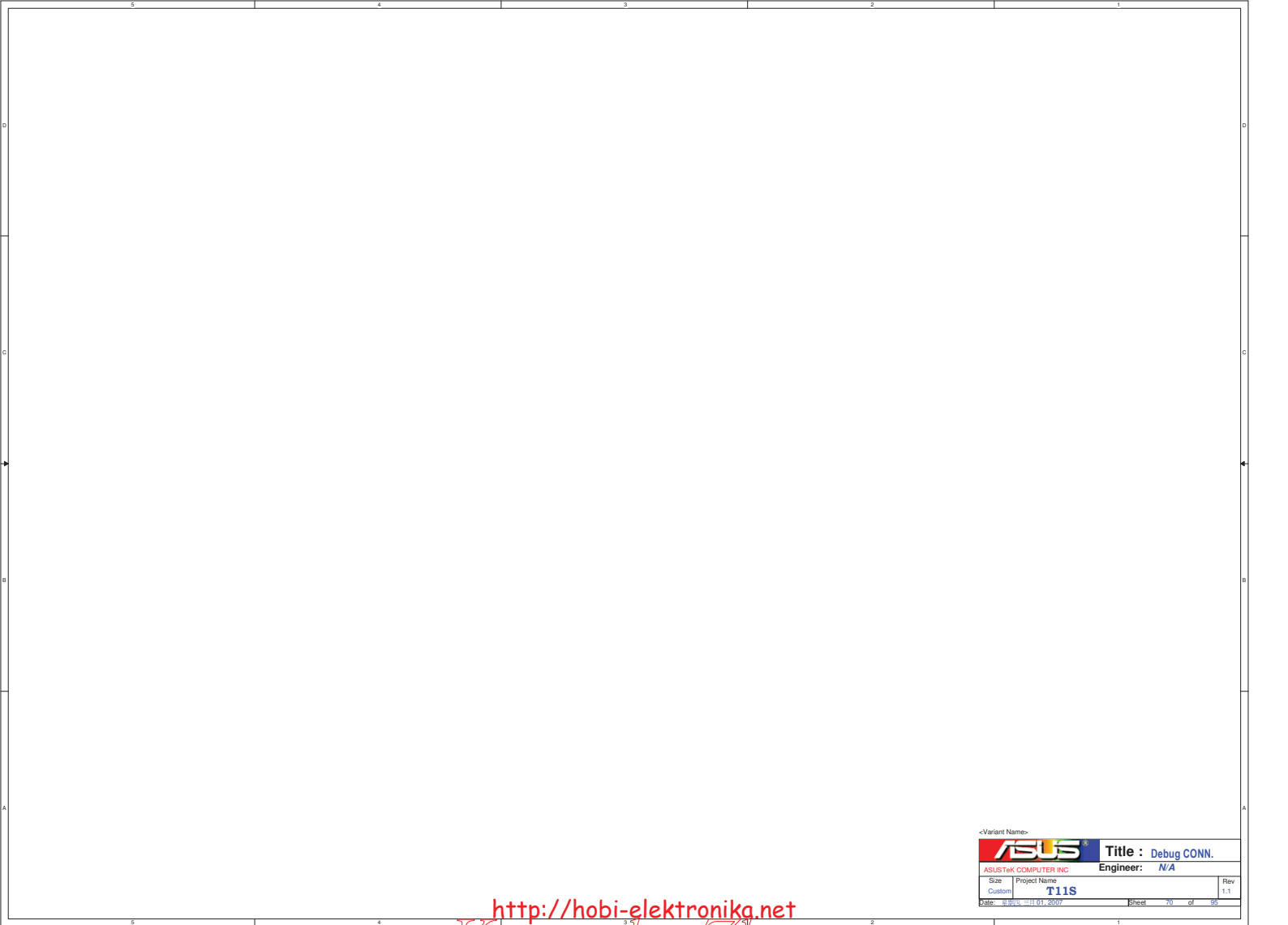
<< Kennedy_Zhang >>



<Variant Name>			
		Title : Schematic page name	
ASUSTek COMPUTER INC		Engineer: N/A	
Size	Project Name		Rev
Custom	T11S		1.1
Date: 2010.01.01		Sheet	69 of 95

<http://hobi-elektronika.net>

<< Kennedy_Zhang >>

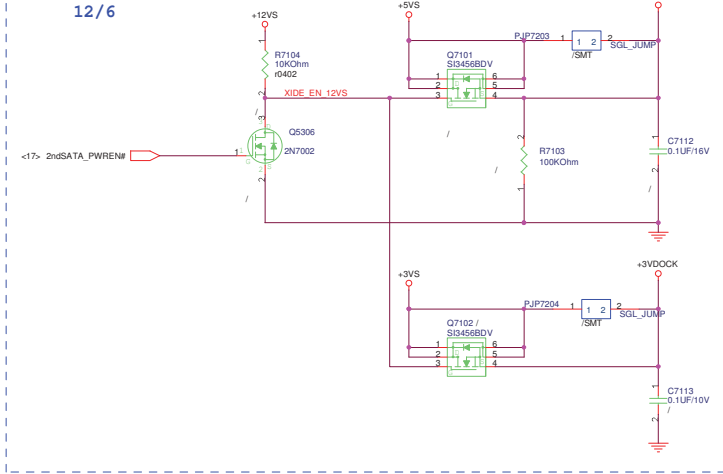


cVariant Name>			
		Title : Debug CONN.	
ASUSTek COMPUTER INC		Engineer: N/A	
Size	Project Name		Rev
Custom	T11S		1.1
Date: 2010.11.01 01:2007		Sheet 70 of 95	

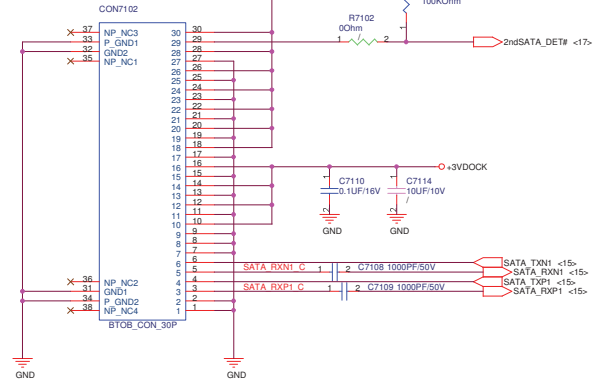
<http://hobi-elektronika.net>

<< Kennedy_Zhang >>

2nd hdd Power (hot plug)
12/6



change PN to 12G160300303

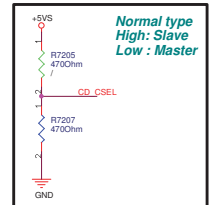
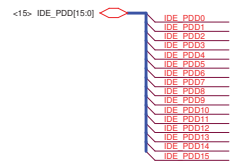


<Variant Name>

ASUS		Title : 2nd_HDD	
ASUSTeK COMPUTER INC.		Engineer: N/A	
Size	Project Name		Rev
Custom	T11S		1.1
Date: 2012.01.01	Sheet	71	of 95

<http://hobi-elektronika.net>
<< Kennedy_Zhang >>

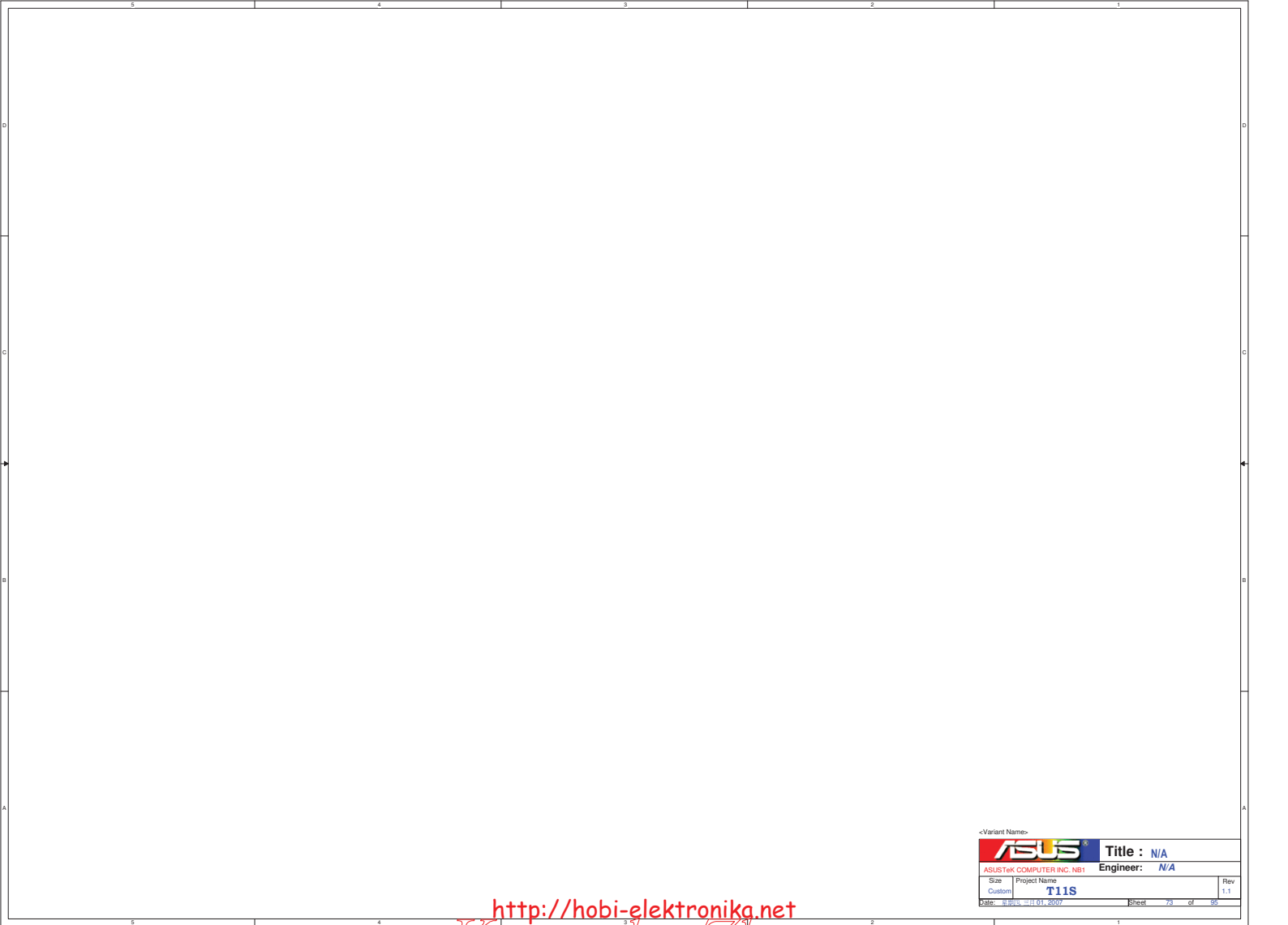
ODD



		Title : SATA-HDD & ODD	
ASUSTeK COMPUTER INC. N01		Engineer: N/A	
Size Custom	Project Name T11S		Rev 1.1
Date: 星期日 01/01/2007		Sheet 72 of 95	

<http://hobi-elektronika.net>

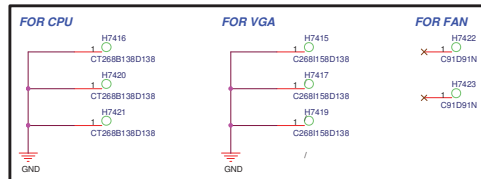
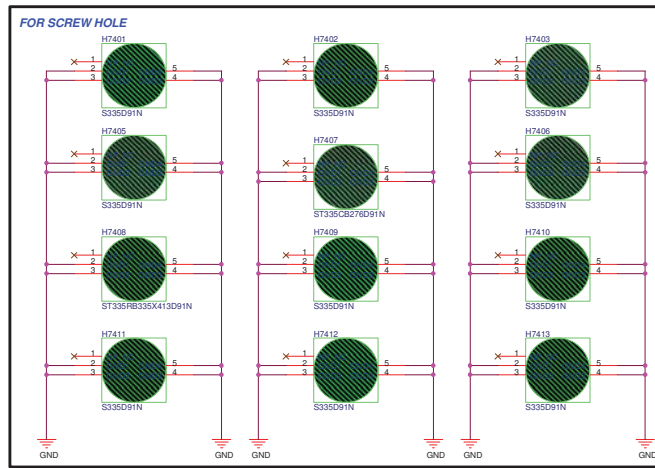
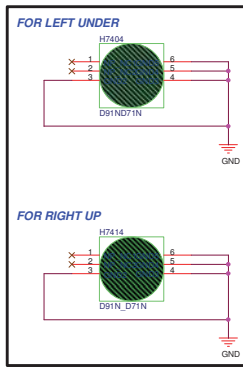
<< Kennedy_Zhang >>



<http://hobi-elektronika.net>

<< Kennedy_Zhang >>

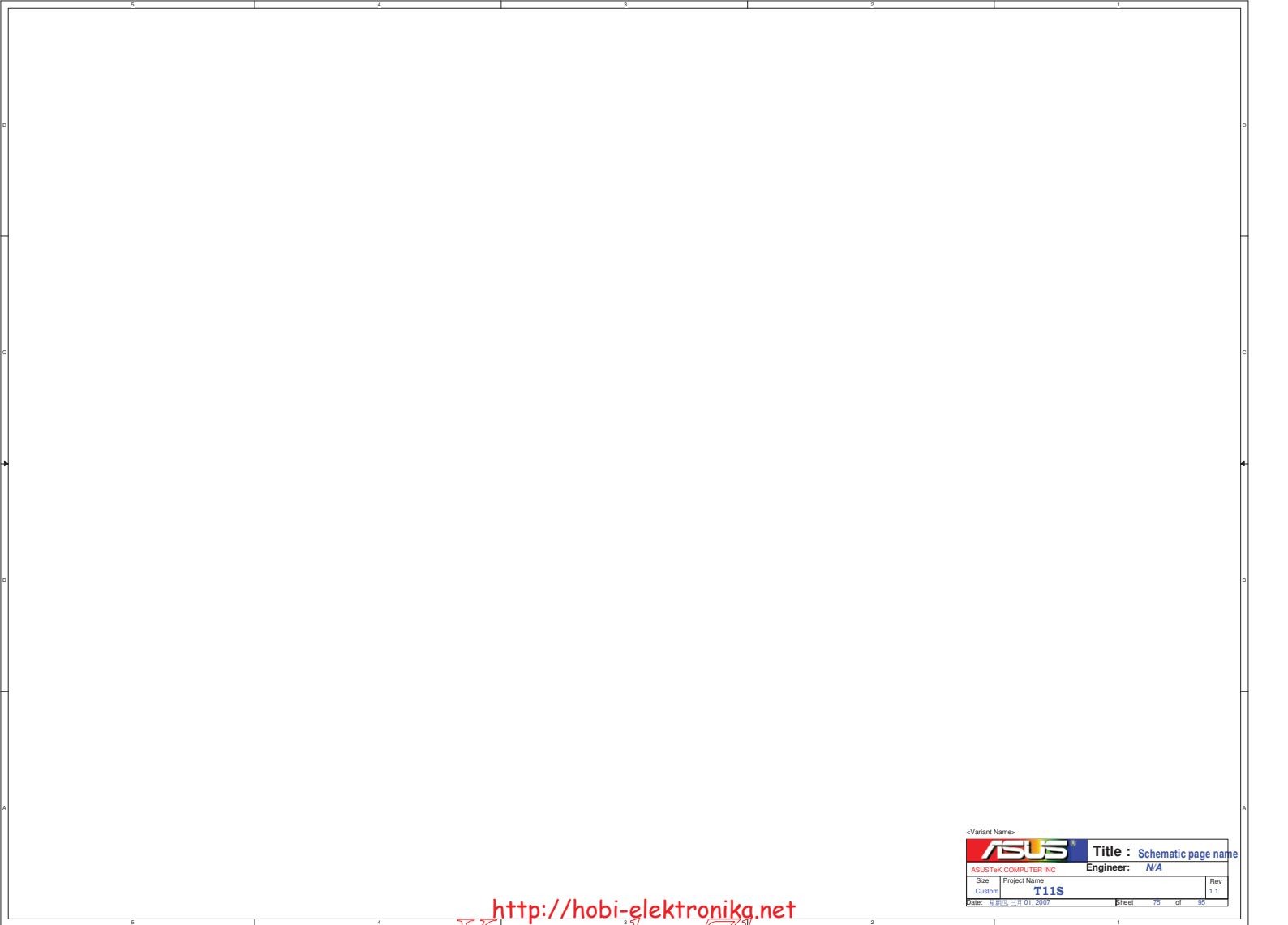
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ASUSTek COMPUTER INC. NB1		Engineer: N/A	
Size	Project Name		Rev
Custom	T11S		1.1
Date: 2010.11.01 01:2007		Sheet	79 of 95



<Variant Name>			
ASUS		Title : SREW HOLE	
ASUSTek COMPUTER INC		Engineer: N/A	
Size	Project Name		Rev
Custom	T11S		1.1
Date: 2010.01.01	Sheet	74	of 95

<http://hobi-elektronika.net>

<< Kennedy_Zhang >>



<http://hobi-elektronika.net>

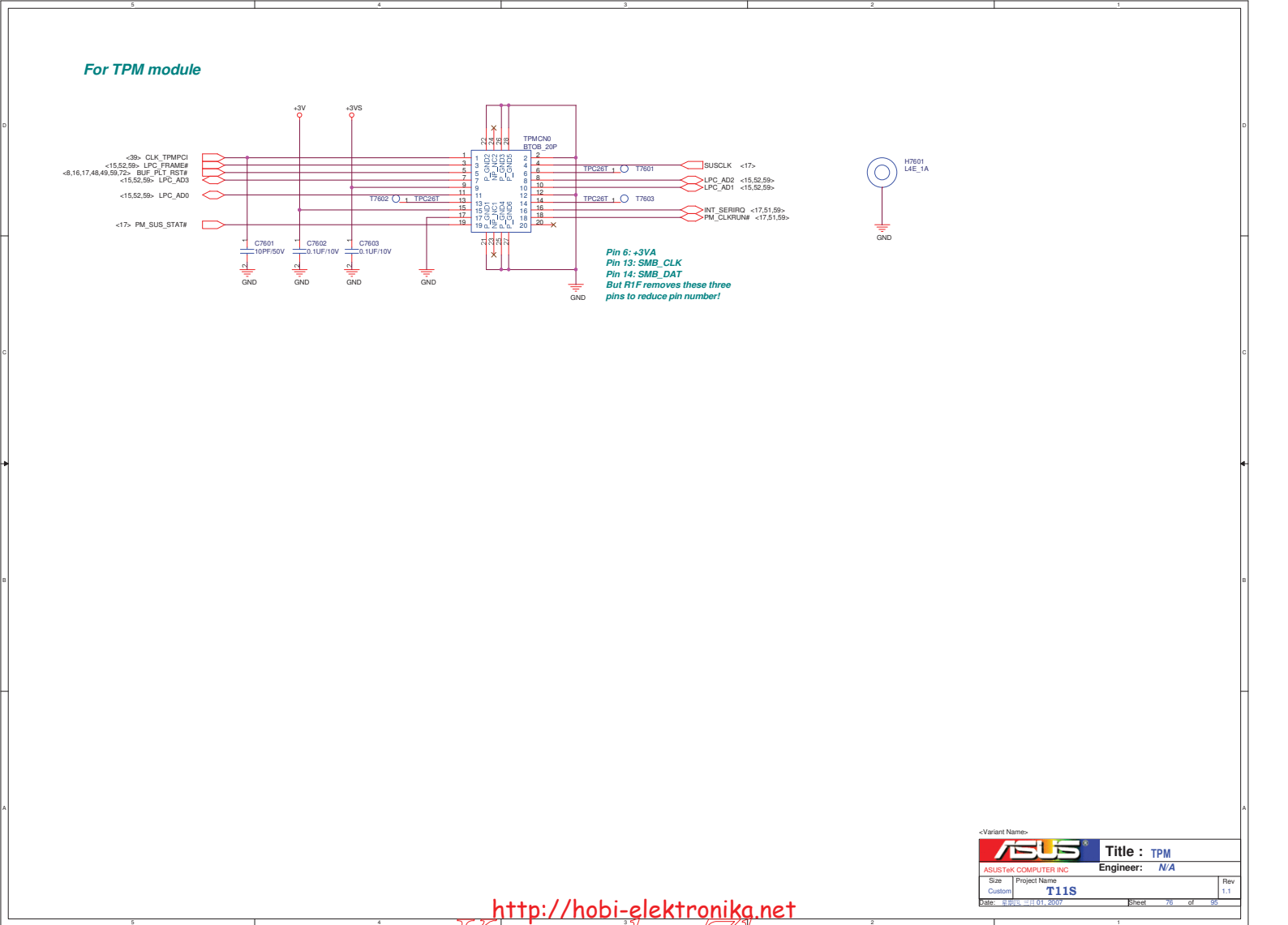
<< Kennedy_Zhang >>

<Variant Name>			
		Title : Schematic page name	
ASUSTeK COMPUTER INC		Engineer: N/A	
Size	Project Name	Rev	
Custom	T11S	1.1	
Date: 2010-01-01 01:2007		Sheet	75 of 95

For TPM module

Pin 6: +3VA
Pin 13: SMB_CLK
Pin 14: SMB_DAT
But R1F removes these three pins to reduce pin number!

ASUS
ASUSTek COMPUTER INC
Size: Custom
Project Name: T11S
Date: 2007.01.01
Sheet: 76 of 95
Title: TPM
Engineer: N/A
Rev: 1.1

[illegible]

For TPM module

Pin 6: +3VA
Pin 13: SMB_CLK
Pin 14: SMB_DAT
But R1F removes these three pins to reduce pin number!

TPM module pins and connections:

- Pin 1: +3V
- Pin 2: +3VS
- Pin 3: GND
- Pin 4: SUSCLK
- Pin 5: LPC_AD0
- Pin 6: LPC_AD1
- Pin 7: LPC_AD2
- Pin 8: LPC_AD3
- Pin 9: LPC_AD4
- Pin 10: LPC_AD5
- Pin 11: LPC_AD6
- Pin 12: LPC_AD7
- Pin 13: LPC_AD8
- Pin 14: LPC_AD9
- Pin 15: LPC_AD10
- Pin 16: LPC_AD11
- Pin 17: LPC_AD12
- Pin 18: LPC_AD13
- Pin 19: LPC_AD14
- Pin 20: GND

Capacitors:

- C7601: 10PF/50V
- C7602: 0.1UF/10V
- C7603: 0.1UF/10V

Other components:

- H7601: L4E_1A
- GND

ASUS Logo

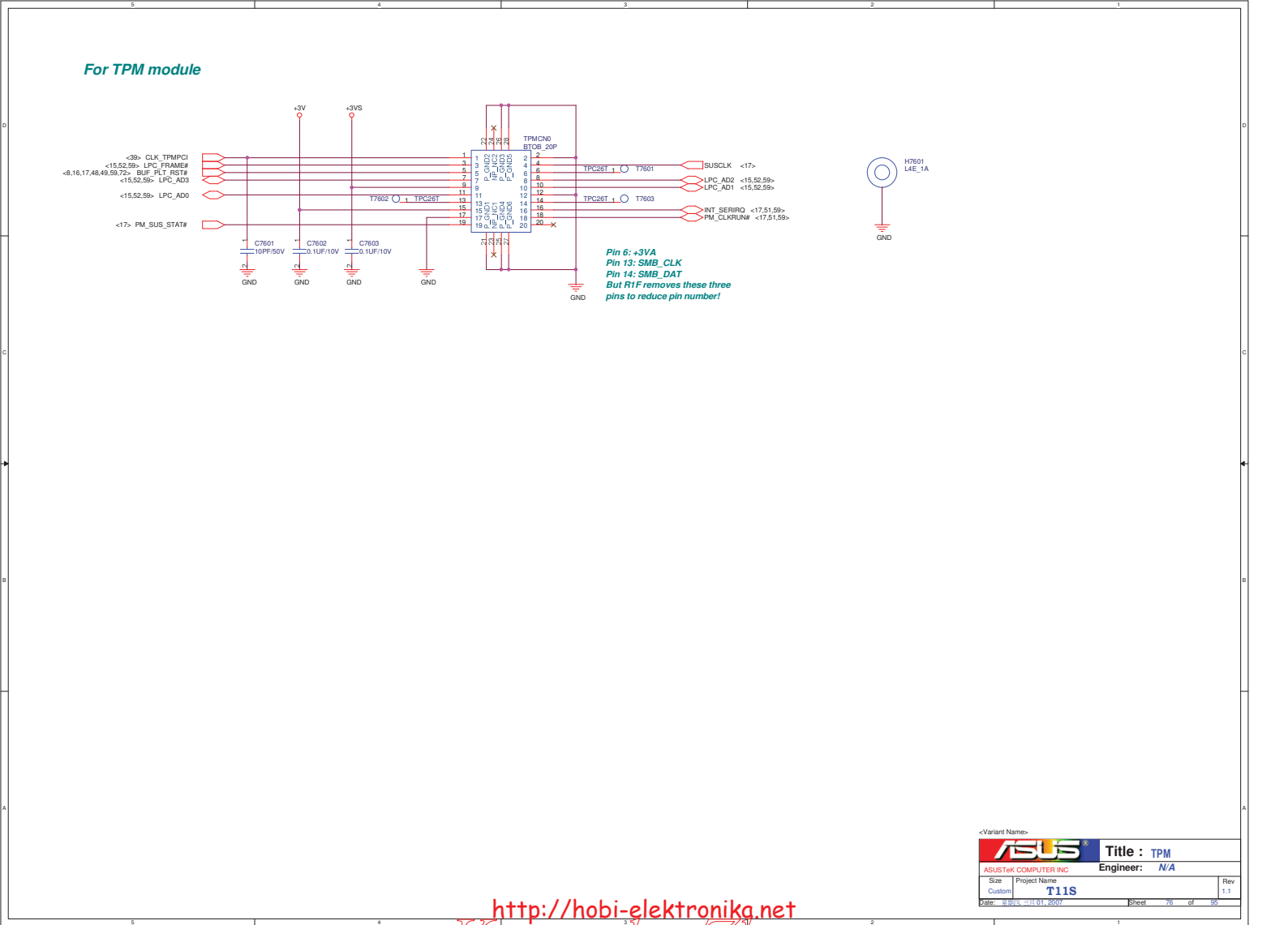
ASUSTek COMPUTER INC. Title : TPM

Engineer: N/A

Size: Custom Project Name: T11S

Date: 2007.01.01 Rev: 1.1

Sheet 76 of 95

[illegible]

For TPM module

Pin 6: +3VA
Pin 13: SMB_CLK
Pin 14: SMB_DAT
But R1F removes these three pins to reduce pin number!

ASUS
ASUSTek COMPUTER INC
Size: Custom
Project Name: T11S
Date: 2007.01.01
Sheet: 76 of 95
Title: TPM
Engineer: N/A
Rev: 1.1

For TPM module

Pin 6: +3VA
Pin 13: SMB_CLK
Pin 14: SMB_DAT
But R1F removes these three pins to reduce pin number!

H7601
L4E_1A

GND

Size		Project Name		Rev
Custom	T11S			1.1

Date: 2007.01.01 Sheet 76 of 95

<http://hobi-elektronika.net>

For TPM module

<3B> CLK_TPMPC1
<15,52,59> LPC_FRAME#
<8,16,17,48,49,59,72> BUF_PLT_RST#
<15,52,59> LPC_AD3
<15,52,59> LPC_AD0
<17> PM_SUS_STAT#

+3V
+3VS
GND
GND
GND
GND
GND
GND

TPMCND BTOR_20P

T7601
T7602
T7603

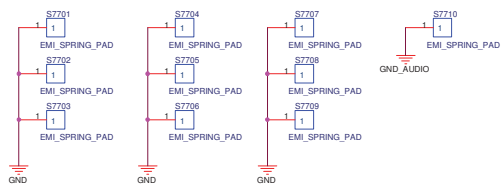
SUSCLK <17>
LPC_AD2 <15,52,59>
LPC_AD1 <15,52,59>
INT_SERIRQ <17,51,59>
PM_CLKRUN# <17,51,59>

Pin 6: +3VA
Pin 13: SMB_CLK
Pin 14: SMB_DAT
But R1F removes these three pins to reduce pin number!

H7601 L4E_1A
GND

<Variant Name>		ASUS		Title : TPM	
ASUSTek COMPUTER INC.		Engineer:		N/A	
Size	Project Name			Rev	
Custom	T11S			1.1	
Date: 2007.01.01	Sheet	76	of	95	

<http://hobi-elektronika.net>

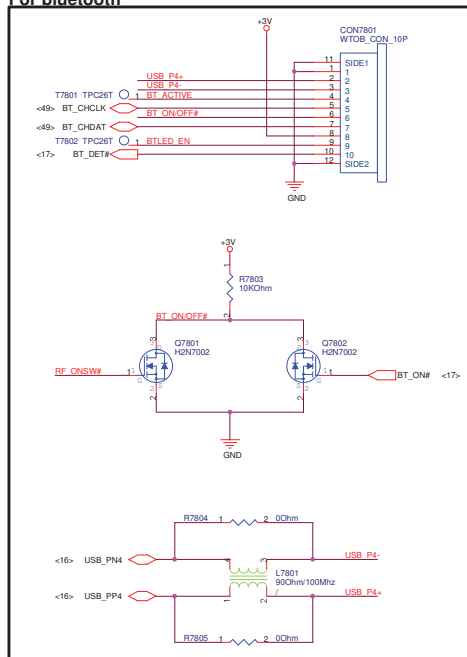


<Variant Name>			ASUS		Title : CIR
ASUSTek COMPUTER INC			Engineer: N/A		
Size	Project Name				Rev
Custom	T11S				1.1
Date: 2010.03.01 01:2007			Sheet	77	of 95

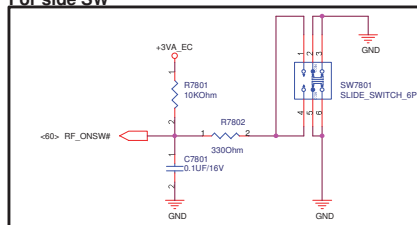
<http://hobi-elektronika.net>

<< Kennedy_Zhang >>

For bluetooth



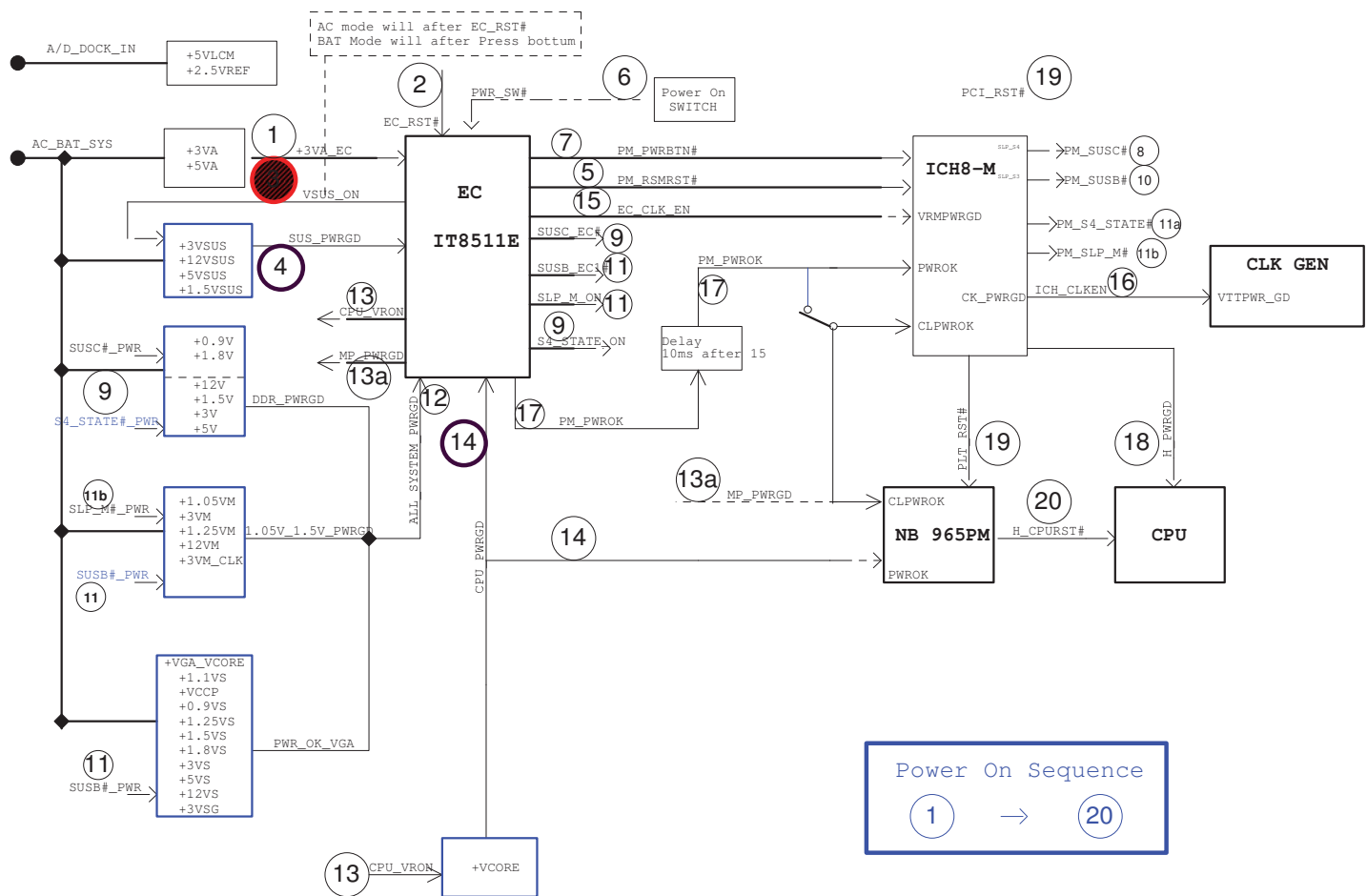
For side SW

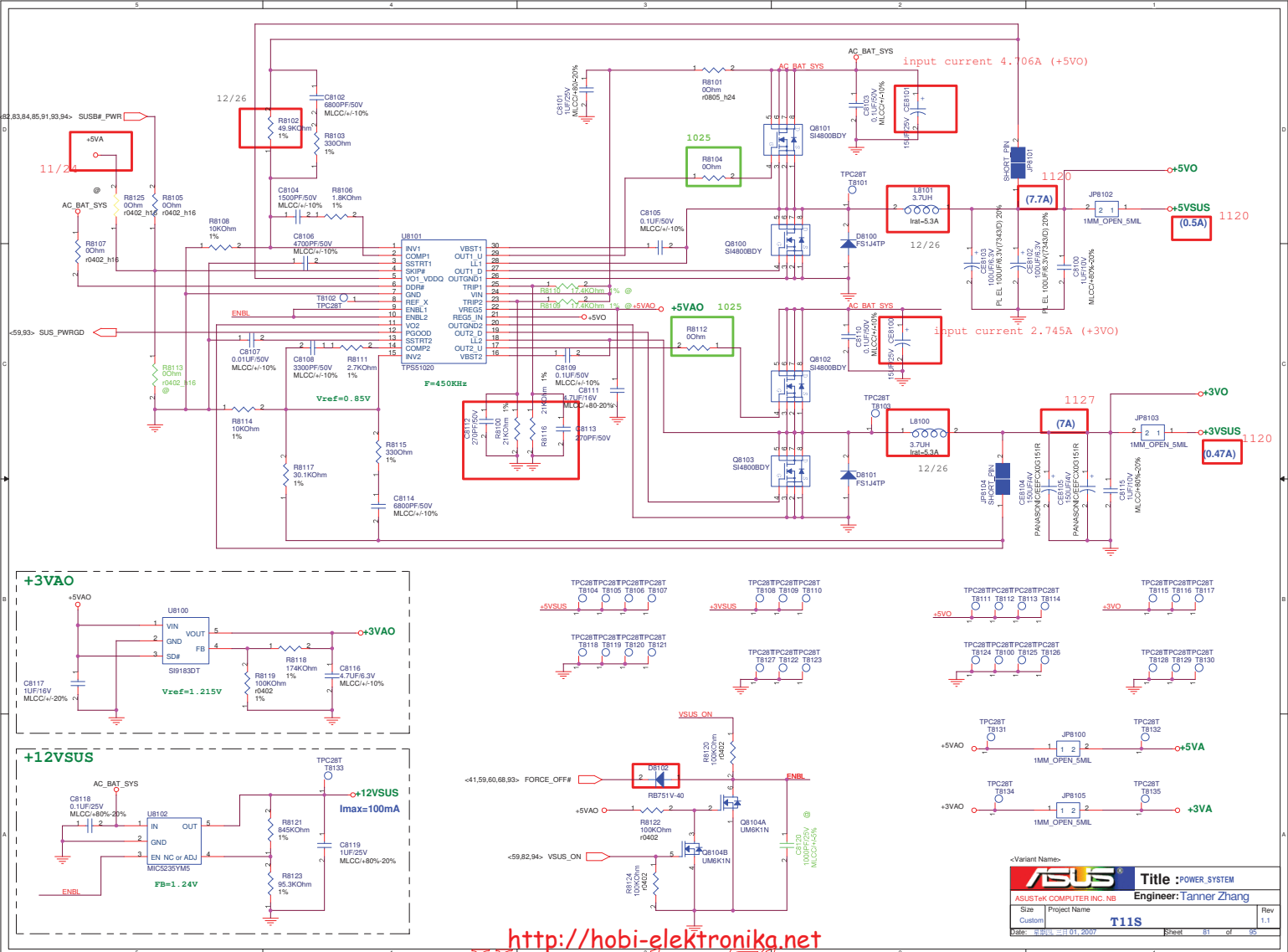


<Variant Name>			
ASUS		Title : Bluetooth	
ASUSTeK COMPUTER INC		Engineer: N/A	
Size	Project Name		Rev
Custom	T11S		1.1
Date: 2013.01.01 01:2007		Sheet	78 of 95

<http://hobi-elektronika.net>

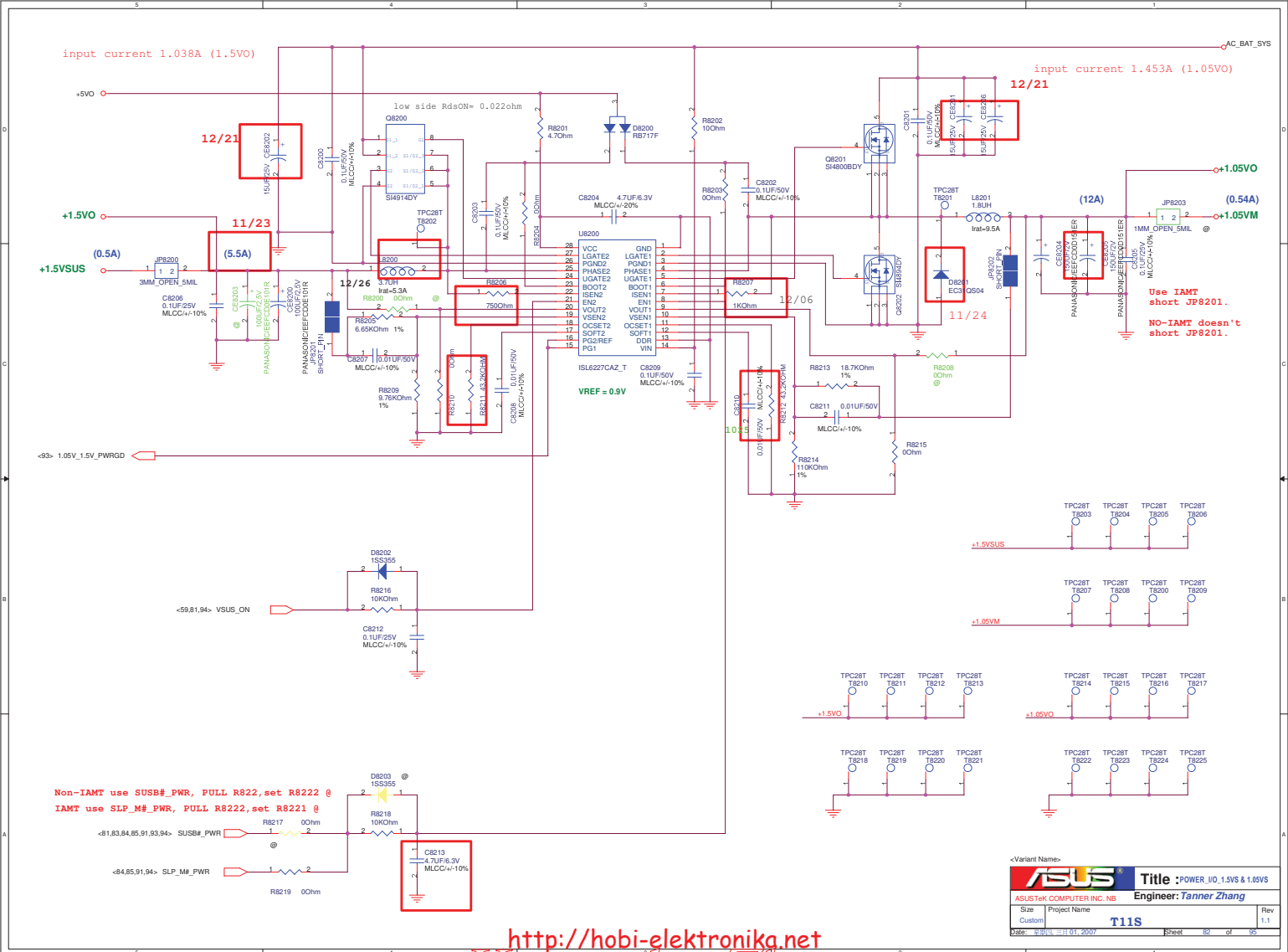
<< Kennedy_Zhang >>





<http://hobi-elektronika.net>

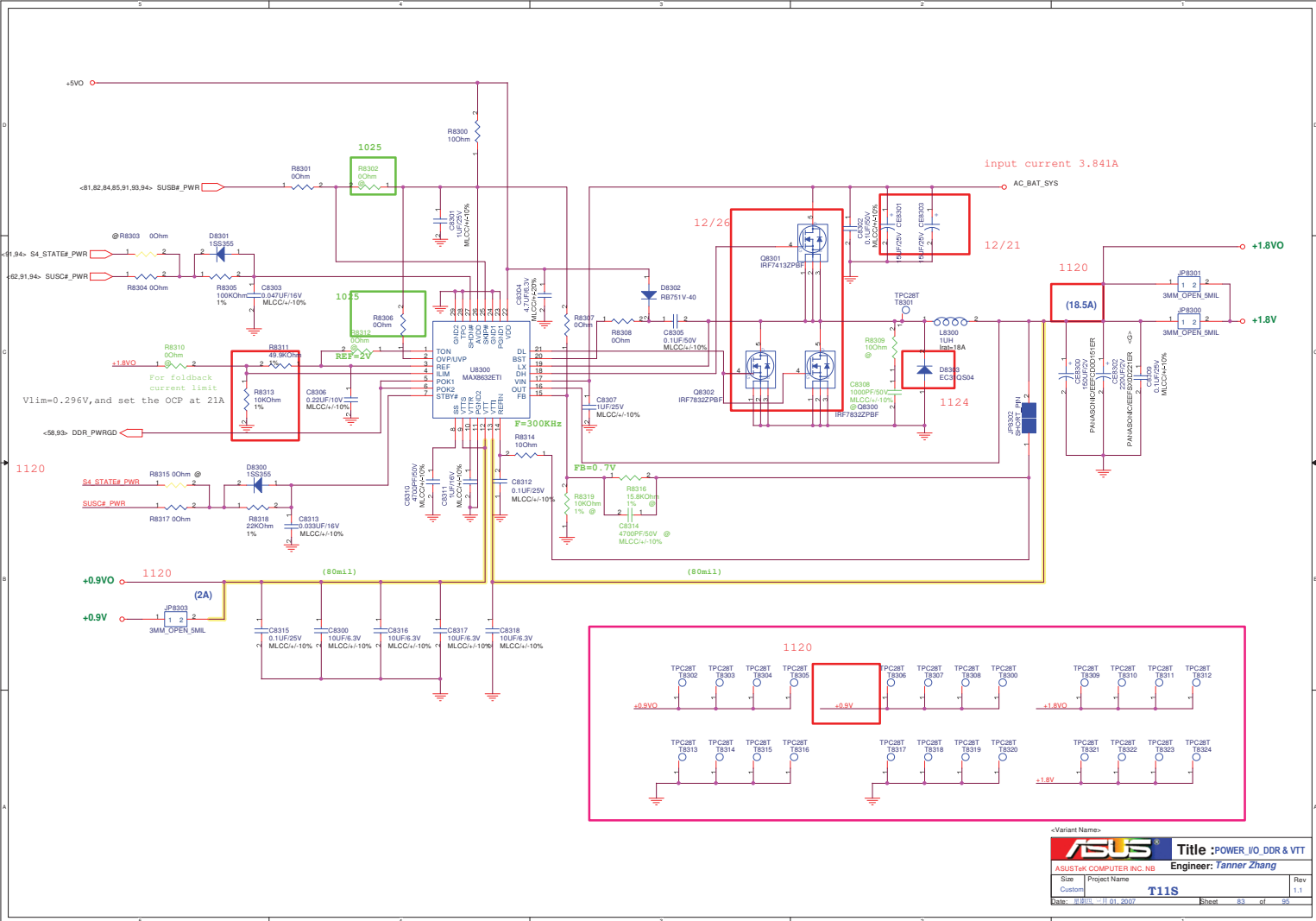
<< Kennedy_Zhang >>



4 3 2

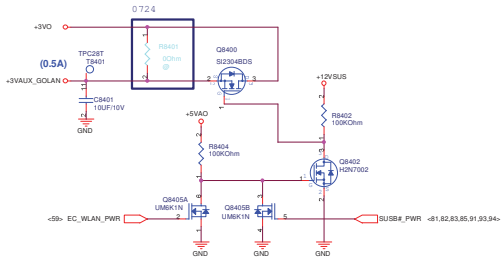
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<< Kennedy_Zhang >>

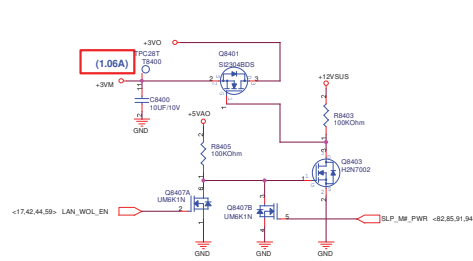


<http://hobi-elektronika.net>
 << Kennedy_Zhang >>

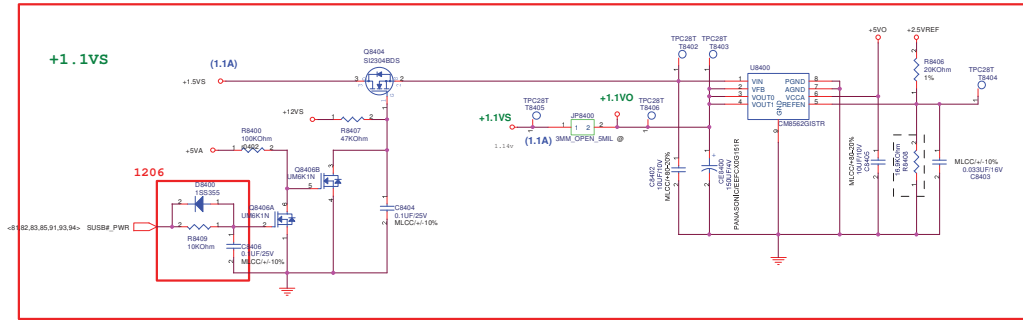
+3VAUX_GOLAN



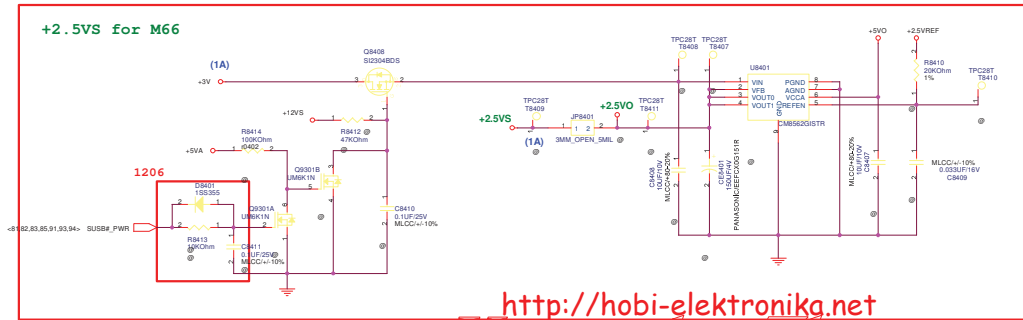
+3VM



+1.1VS



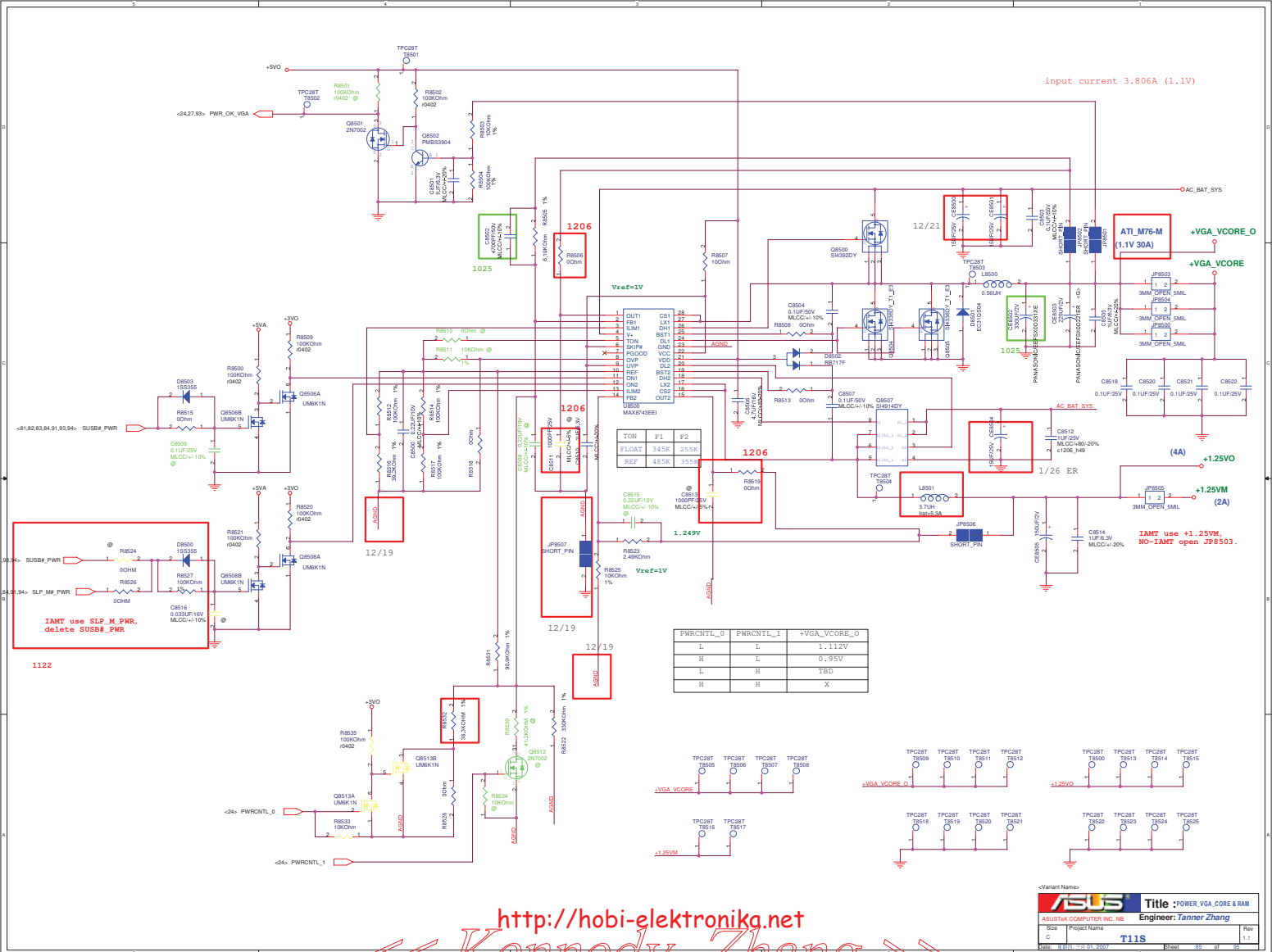
+2.5VS for M66



<http://hobi-elektronikg.net>

<< Kennedy_Zhang >>

ASUS		Title: POWER_IO_+3VM & +1.1VS	
Engineer: Tanner Zhang			
Size	Project Name	Rev	
1	T11S	1.1	
Date: 2014-11-27-2017	Sheet: 84	of	85



<http://hobi-elektronikg.net>

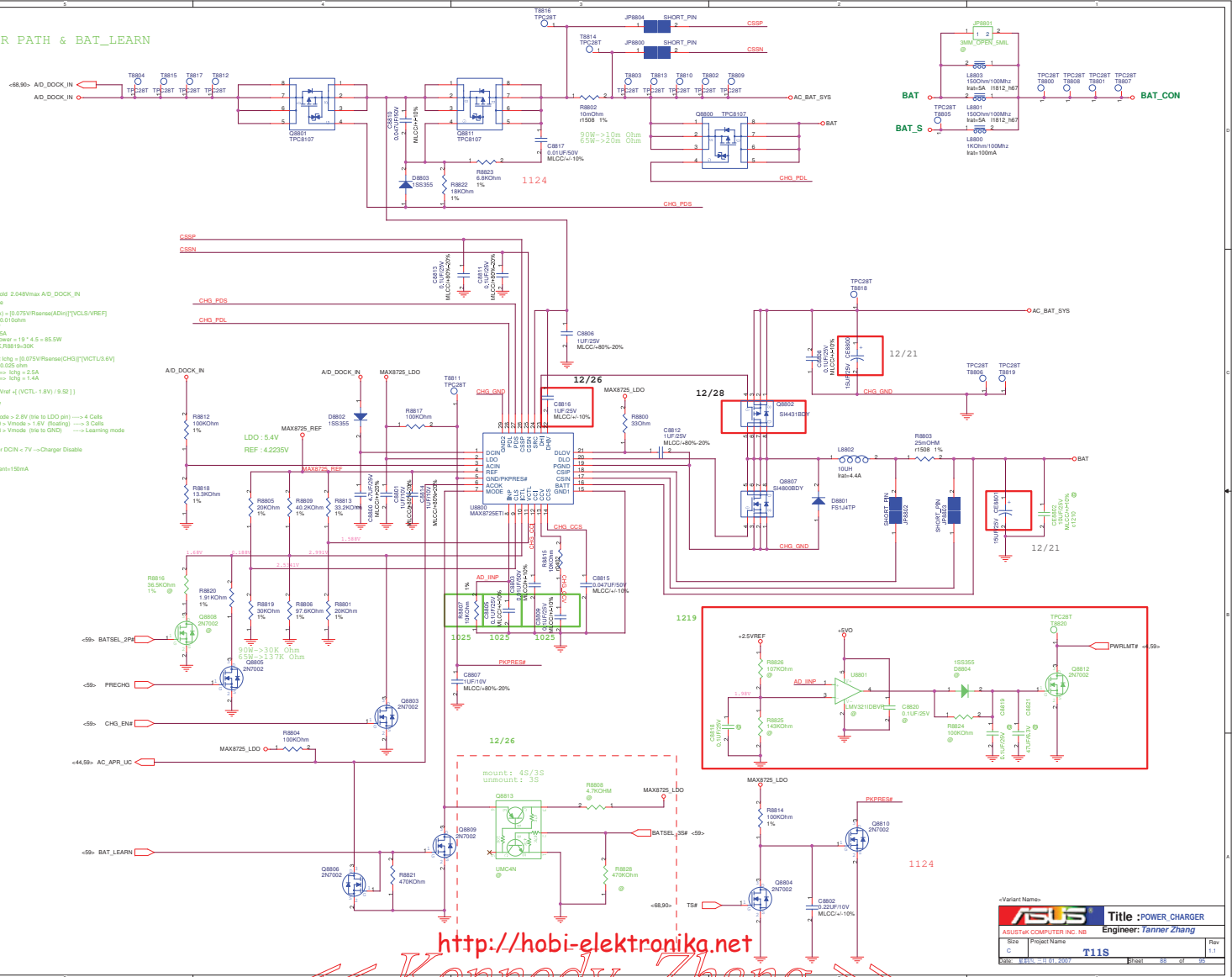
http://mod-electronics.net
 << Kennedy_Zhang >>

<http://hobi-elektronikg.net>

« Kennedy_Zhang »

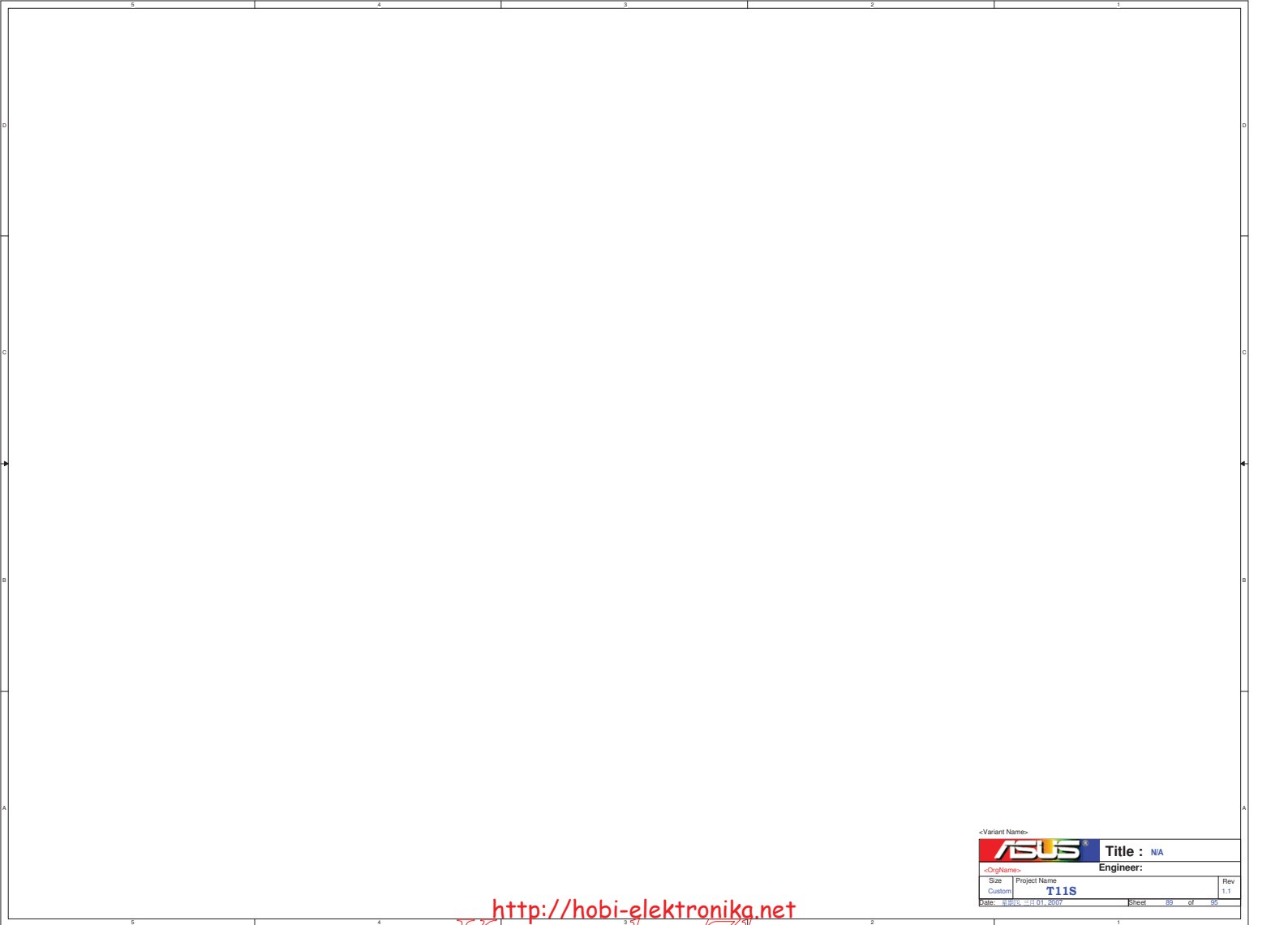
<Variant Name>		Title : NA	
<Original Name>		Engineer:	
Size	Project Name		Rev
C	T11S		1.1
Date: 11/11/2007		Drawn By	df

POWER PATH & BAT_LEARN



<http://hobi-elektronikg.net>

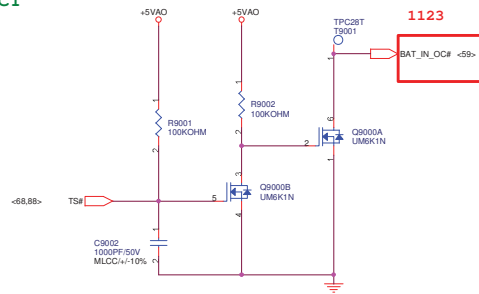
http://noble-electronics.net
 << Kennedy_Zhang >>



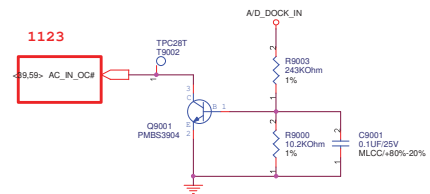
<http://hobi-elektronika.net>

<< Kennedy_Zhang >>

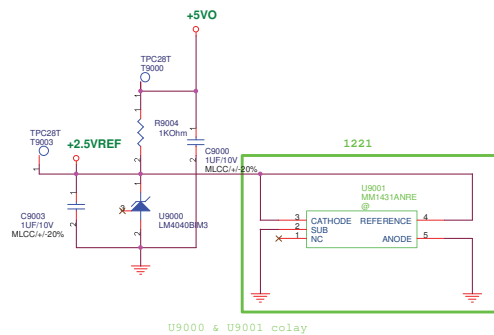
BATTERY IN DETECT



ADAPTER IN DETECT



+2.5VREF

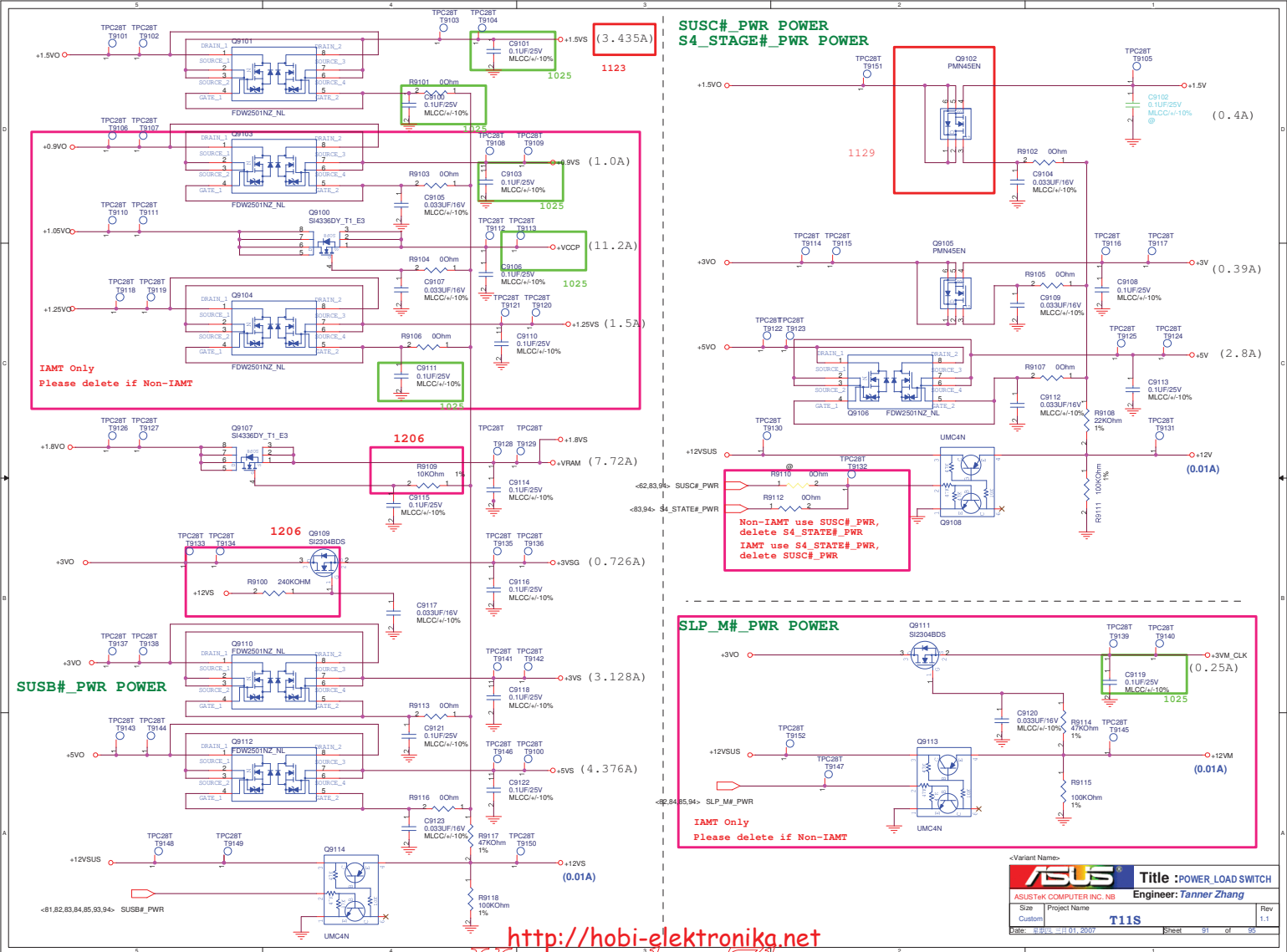


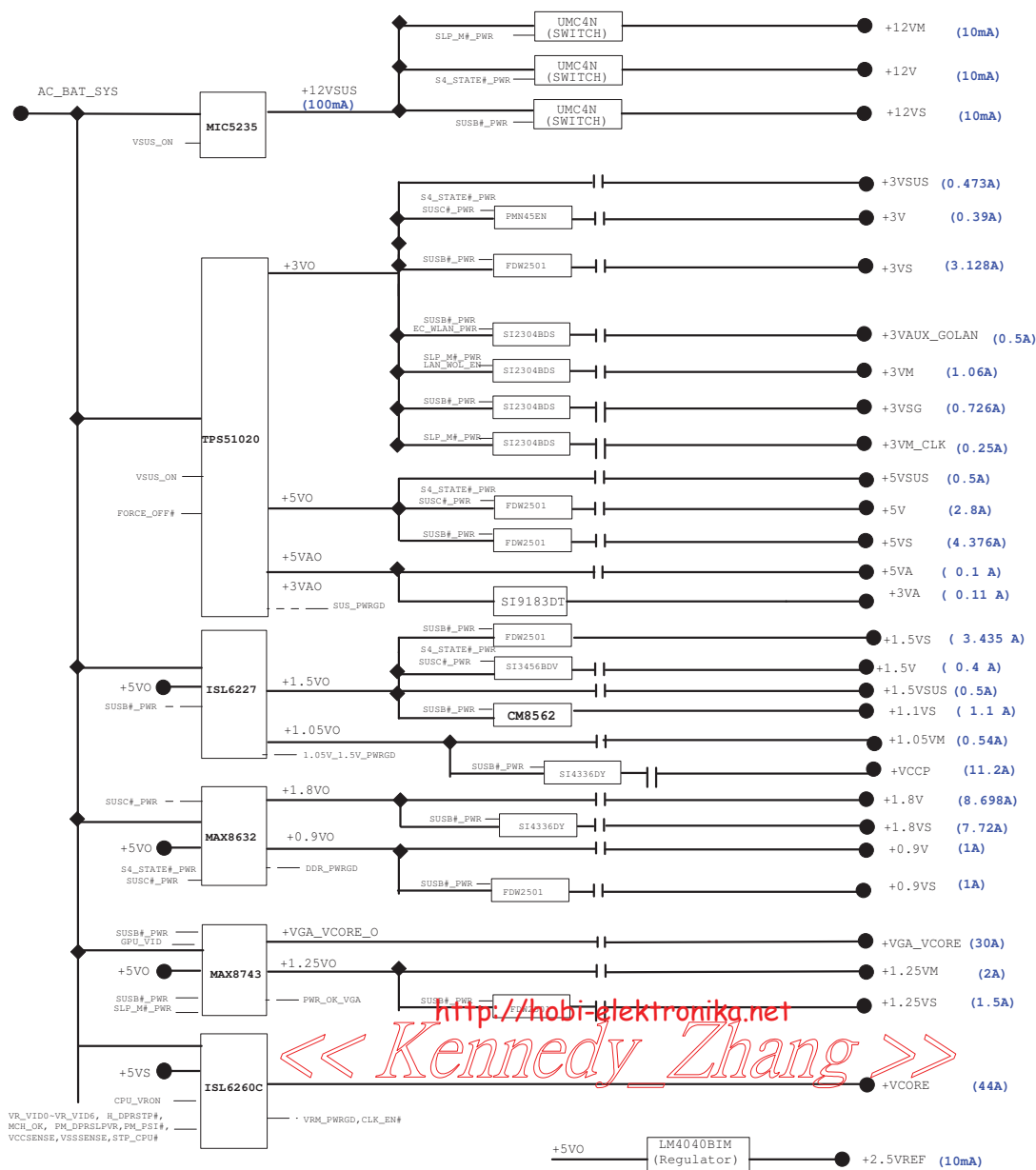
U9000 & U9001 colay

<Variant Name>			
ASUS		Title : POWER_DETECT	
<OrigName>		Engineer: Tanner Zhang	
Size	Project Name	Rev	
Custom	T11S	1.1	
Date: 2010.01.01 01:2007		Sheet	90 of 95

<http://hobi-elektronika.net>

<< Kennedy_Zhang >>



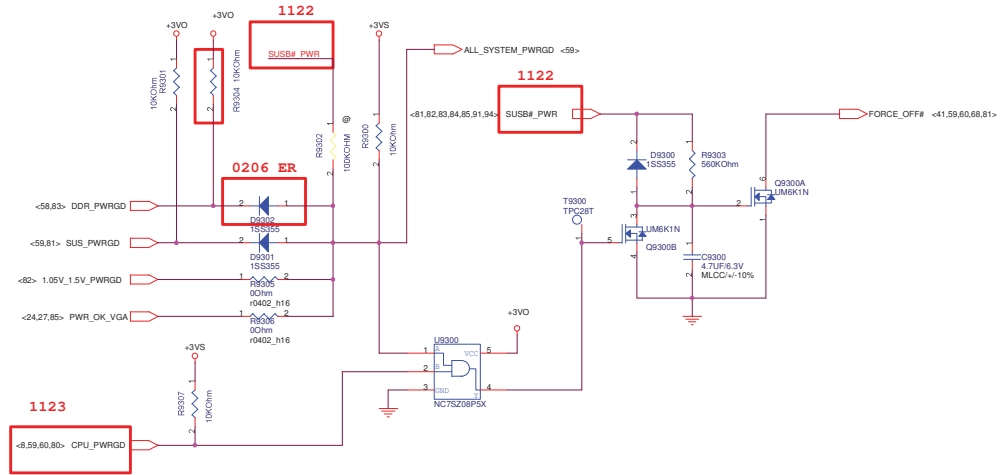


<http://hobi-elektronika.net>

<< Kennedy Zhang >>

ASUS		Title : POWER_VCORE	
Engineer: Tanner Zhang			
Size	Project Name	Rev	
Control	T11S	1.1	
Rev	1.1	1.1	

POWER GOOD DETECTER



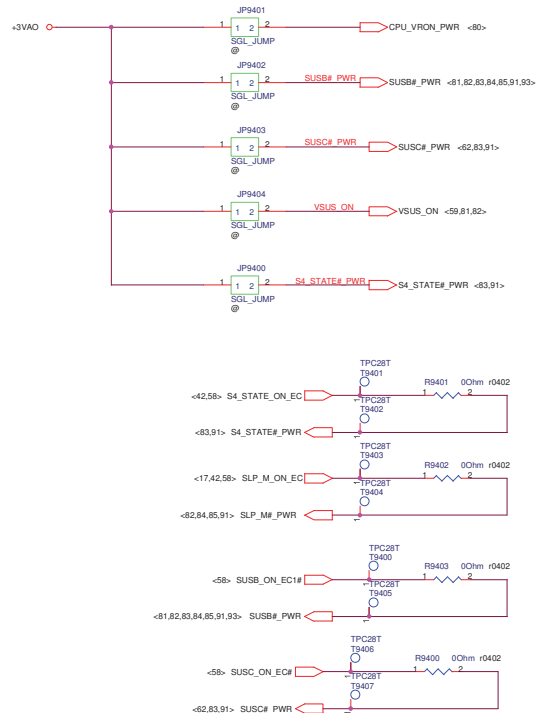
<Variant Name>		ASUS		Title : POWER_PROTECT	
<OrigName>		Engineer: Tanner Zhang			
Size	Project Name	T11S		Rev	
Custom				1.1	
Date: 2010.01.01		Sheet 89 of 95			

<http://hobi-elektronika.net>

<< Kennedy_Zhang >>

FOR POWER TEST

AC_BAT_SYS	AC_BAT_SYS	<33,68,80,81,82,83,85,88>
+3VA	+3VA	<15,17,33,41,42,44,58,59,60,62,81>
+5VA	+5VA	<81,84,85>
+5VAO	+5VAO	<81,84,90>
+5VO	+5VO	<66,81,82,83,84,85,88,90,91>
+3VO	+3VO	<81,84,85,91,93>
+3VSUS	+3VSUS	<16,17,18,44,45,58,59,81>
+5VSUS	+5VSUS	<17,18,66,81>
+3V	+3V	<33,42,44,45,48,49,51,52,53,76,78,84,91>
+3VS	+3VS	<4,8,12,15,16,17,18,31,32,33,35,37,42,48,49,51,53,56,57,59,60,65,66,71,72,76,80,91,93>
+3VM	+3VM	<17,18,20,21,38,42,44,49,58,85,84>
+3VALUX_GOLAN	+3VALUX_GOLAN	<48,49,84>
+3VM_CLK	+3VM_CLK	<39,42,91>
+12VSUS	+12VSUS	<17,62,81,84,91>
+12V	+12V	<42,53,57,91>
+12VS	+12VS	<27,32,33,57,66,71,84,91>
+5V	+5V	<33,42,52,62,91>
+5VS	+5VS	<17,18,32,35,37,42,56,57,61,66,71,72,80,91>
+1.1VS	+1.1VS	<24,26,27,42,84>
+1.8VS	+1.8VS	<24,25,27,28,42,91>
+1.8VO	+1.8VO	<83,91>
+1.8V	+1.8V	<8,11,12,20,21,42,83>
+VCCP	+VCCP	<4,5,7,9,11,12,15,18,39,42,91>
+1.05VM	+1.05VM	<11,42,58,82>
+0.9VO	+0.9VO	<83,91>
+0.9V	+0.9V	<22,42,83>
+0.9VS	+0.9VS	<28,29,42,91>
BAT	BAT	<8>
+2.5VREF	+2.5VREF	<84,88,90>
+VCCORE	+VCCORE	<5,80>
+VGA_VCORE	+VGA_VCORE	<24,27,85>
+VRAM	+VRAM	<24,25,27,28,29,42,91>
+1.25VS	+1.25VS	<12,18,42,91>
+1.25VM	+1.25VM	<9,12,42,85>
BAT_CON	BAT_CON	<68,88>
+3VSG	+3VSG	<24,27,30,91>
+12VM	+12VM	<91>



<Variant Name>	
	
Title : POWER SIGNAL	
Engineer: Tanner Zhang	
Size	Project Name
Custom	T11S
Date: 2007.11.01	Sheet 94 of 95
Rev 1.1	

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<< Kennedy_Zhang >>

Rev	Date	Description
1.0	2006/11/	1. Initial release.
1.1	2007/02/09	1.WLAN_ON#(GPIO17) change to +3VS pull High (page 17) 2.R5950,R5920 delte,Delete Q3901,Q3902 (page 59,39) 3.D6201 Spin power 改在bead 后 (page 62 layout request) 4.Delete D5102 (page 51) 5.82566mm errata:add 10nf between +3V and 1.8vctrl and +1.0Vctrl (page 44) 6.R2460,R2422,R2426 unstuff,R2323 pull Down (page 23,24) 7.Add C2003,CN2201,CN2202,CN2206 (page 20,22) 8.SPI ROM P/N change to:05G001405010 (page 65) 9.ADD +3VM&+1.05VM POWER OK SIGNAL, add HW optional MP_PWRGD & LAN_RST# ciucuit.(page 58) 10.modify the Keyboard Matrix (page 61) 11.add GPU VID control 1(page 24) 12.SB GPIO12 change from "EXT_SCI"# to "LAN_PHYPC" for AMT (page 17) optional 13.Change C2940 to0.1uF.[28] 14.R1761 LAN_WOL_EN Pull high to +3VSUS Optional (page 17) 15.R3952 change to 270 ohm 1% (page 39) 16.delete LPC debug circuit(page 70) 17.unstuff the +1.05VM discharge circuit.(page 42)

Rev	Date	Description

<Variant Name>		ASUS		Title : History	
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